



SRI VASAVI ENGINEERING COLLEGE
(Approved by AICTE, New Delhi & Affiliated to JNTU, Kakinada)
Pedatadepalli, TADEPALLIGUDEM-534 101, W.G. Dist., AP

Accredited

NBA

**DEPARTMENT OF ELECTRONICS & COMMUNICATION
ENGINEERING**

**Papers Published in National/International Journals
(2014-2015)**

S. No.	Name of the staff member	Title of the paper	Name of the published journal and year	Impact factor
1	Dr.E.Kusuma kumari	Design and Analysis of Inset Feed Rectangular Microstrip Patch Antenna with and without Fractal Geometry for wideband Applications	International Journal of Engineering Technology Science and Research(IJETS), ISSN 2394 – 3386 March 2015	2.120
2	P.Gopala Reddy	MDC FFT /IFFT Processor with Variable length for MIMO OFDM Systems	International Journal of Advanced Technology & Innovative Research(IJATIR), ISSN 2348-2370 January-2015	3.214
3	G.Shankara Bhaskara Rao	Simulation of Models &BER Performances of DWT-OFDM Vs DAUBECHIES-OFDM	International Journal of Innovative Research &Development (IJIRD), ISSN 2278-0211(online) December-2014	0.765
4	D.Rajesh Setty	Image Characterization Bsed Adaptive Wavelet Transform in Image Retrieval	International Journal for Research in Applied Science &Engineering technology (IJRASET), December-2014	0.435
5	M.Satish Kumar	ARM7 Microcontroller based Robot Controlled by an Android mobile utilizing Bluetooth	International Journal of Innovative Research in Technology (IJIRT),ISSN:2349-6002 2014	2.1
6	Thota Srinivas	Advanced Panel Splitting for non uniform illumination Images by using Transformation Technique	International Journal of Innovative Research in Technology (IJIRT), ISSN:2349-6002 2014	2.1
7	A.P.Ramesh	Implementation of Integer Square Root	International Journal of Engineering Science and Innovative Research(IJESIT), ISSN 2319 – 5967, 18-Dec- 2014	1.913

8	K.N.H.Srinivas	An Efficient Technique for parallel CRC generation	International Journal of Engineering and Computer Science(IJECS),ISSN:2319-7242,12-Dec-2014	3.093
9	M.Koteswararao	Finger print verification using Holistic and nonlinear score level combinations	International Journal of Science, Engineering and Technology Research(IJSETR), ISSN:2319-8885, VOL 3,ISSUE:49,Dec-2014	3.59
10	Y. Sujatha	High performance SRAM design for low power applications	International Journal of Science, Engineering and Technology Research(IJSETR), ISSN:2319-8885, VOL 3,ISSUE:45,Dec-2014	3.59
11	T. Sreenivas	Portable camera based assistive systems for product label reading with voice output for blind people	International Journal and magazine of Engineering, Technology, Management and research(IJMETMR),ISSN:2348-4845, VOL-1, Issue-11,Nov-2014	2.6250
12	Dr.E. Kusuma Kumari	New D-Flip- flop with low power clocking using stacking method	International Journal of Scientific Engineering and Technology Research(IJSETR), ISSN:2319-8885, VOL 3,ISSUE:42,Nov-2014	3.59
13	D. Rajesh Setty	Design and development of PIC microcontroller with RF based toll gate management system	International Journal of Engineering Research and Technology(IJERT), ISSN:2278-0181, VOL-3,Issue 11,29-Nov-2014	1.76
14	D. R. Sandeep	Low power 32 bit DADDA multiplier	International Journal of computer trends and Technology(IJCTT),ISSN:231-5381, VOL-17,Nov-2014	1.517
15	K. Vijaya Lakshmi	Low power 32 bit DADDA multiplier	International Journal of computer trends and Technology(IJCTT),ISSN:231-5381, VOL-17,Nov-2014	1.517
16	Dr.E.Kusuma Kumari	A Broad band Planar Micro Strip Antenna with Meta Materials	International Journal of Research Studies in Computer Science and Engineering (IJRSCSE) ISSN 2349-4840 (print) &ISSN 2349-4859 (online) July 2014	2.108