



SRI VASAVI ENGINEERING COLLEGE
(Approved by AICTE, New Delhi & Affiliated to JNTU, Kakinada)
Pedatadepalli, TADEPALLIGUDEM-534 101, W.G. Dist., AP

Accredited

NBA

**DEPARTMENT OF ELECTRONICS & COMMUNICATION
ENGINEERING**

**Papers Published in National/International Journals
(2013-2014)**

S.No	Name of the Staff Member	Title of the Paper	Name of the Journal & Published year	Impact Factor
1.	K.N.H.Srinivas	Power Efficient Low Latency Carry Select Adder	International Journal of Electronics communication Technology(IJECT),March- 2014, ISSN:2230-9543	0.675
2.	Y.Sujatha	Improved Watermarking algorithm for securing Image authentication and Integrity.	International Journal of Computer Trends and Technology (IJCTT)- volume 8 Number-Feb-2014.ISSN:2231-2803.	1.517
3.	Thota.Srinivas	An Efficient Test Data Compression Using Viterbi Algorithm.	International Journal of Science, Engineering and Technology Research(IJSETR) volume 2,Issue 12, Dec-2013. ISSN: 2278-7798	3.59
4.	M. Koteswara Rao	Voice Verification System using Symlet Wavelet	International Journal of Research in computer applications and Robotics (IJRCAR),Vol1,Issue 9.Dec-2013,ISSN:2320-7345	2.497
5.	D. Rajesh Setty	Advance MEMS For Unwired Input Operations for Future Electronic Machines	International Journal of Reviews on Recent Electronics and Computer Science(IJRRECS),Vol1,Issue-7,Nov-2013,ISSN 2321-5461	1.53
6.	M. Kishore Kumar	Design and Implementation of supervising Robot by using SMS	International Journal of Advanced Research in Electrical,Electronics and Instrumentation Engineering(IJAREEIE),vol2,Issue11,Nov-2013,ISSN-2320-3765	5.016
7.	D.R.Sandeep	Implementation of Billing System,Theft Control and Accident notification systems Based on GSM and GPS Technologies	International Journal of Engineering Research & Technology(IJERT),ISSN:2278-0181,vol 2,Issue11,Nov-2013	1.76

8.	A.P.Ramesh	Floating Point Multiplier using Canonical Signed Digit	International Journal of Advanced Research in Electronics and communication Engineering(IJARECE),vol2,Issue11,Nov-2013,ISSN:2278-909X	4.295
9.	A.P.Ramesh	Asynchronous Single Precision Floating Point Multiplier using Verilog HDL	International Journal of Advanced Research in Electronics and communication Engineering(IJARECE),vol2,Issue11,Nov-2013,ISSN:2278-909X	4.295