



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

RESULTS Of II B.Tech I SEMESTER (R13/R10) Supplementary Examinations, July-2022

College name: VASAVI,TADEPALLIGUDEM:A8

| Htno       | Subcode | Subname                                  | Internal | External | CREDITS |
|------------|---------|--|----------|----------|---------|
| 12A81A0413 | R21022  | MANAGERIAL ECONOMICS AND FINANCIAL ANALY | 12       | -1       | 0       |
| 12A81A0413 | R21026  | ELECTRONIC DEVICES AND CIRCUITS          | 8        | -1       | 0       |
| 12A81A0413 | R21041  | ELECTRICAL TECHNOLOGY                    | 0        | -1       | 0       |
| 12A81A0413 | R21042  | NETWORK ANALYSIS                         | 0        | -1       | 0       |
| 12A81A0413 | R21043  | PROBABILITY THEORY & STOCHASTIC PROCESSE | 0        | -1       | 0       |
| 12A81A0413 | R21044  | SIGNALS & SYSTEMS                        | 0        | -1       | 0       |
| 14A81A0323 | RT21011 | BASIC ELECTRICAL & ELECTRONICS ENGINEERI | 12       | 35       | 3       |
| 14A81A0323 | RT21032 | MECHANICS OF SOLIDS                      | 10       | 44       | 3       |
| 14A81A0477 | RT21042 | DATA STRUCTURES                          | 9        | -1       | 0       |
| 14A81A0477 | RT21044 | SIGNALS AND SYSTEMS                      | 8        | -1       | 0       |
| 14A81A0477 | RT21045 | ELECTRICAL TECHNOLOGY                    | 11       | -1       | 0       |
| 14A81A0494 | RT21044 | SIGNALS AND SYSTEMS                      | 12       | 16       | 0       |
| 15A81A0108 | RT21011 | BASIC ELECTRICAL & ELECTRONICS ENGINEERI | 16       | -1       | 0       |
| 15A81A0108 | RT21012 | PROBABILITY & STATISTICS                 | 16       | -1       | 0       |
| 15A81A0108 | RT21013 | STRENGTH OF MATERIALS-I                  | 12       | -1       | 0       |
| 15A81A0108 | RT21016 | FLUID MECHANICS                          | 11       | -1       | 0       |
| 15A81A0204 | RT21021 | ELECTRICAL CIRCUIT ANALYSIS-II           | 11       | -1       | 0       |
| 15A81A0379 | RT21032 | MECHANICS OF SOLIDS                      | 16       | 41       | 3       |
| 15A81A0393 | RT21011 | BASIC ELECTRICAL & ELECTRONICS ENGINEERI | 18       | 7        | 0       |
| 15A81A0420 | RT21044 | SIGNALS AND SYSTEMS                      | 17       | 18       | 0       |
| 15A81A0474 | RT21044 | SIGNALS AND SYSTEMS                      | 13       | 31       | 3       |
| 15A81A0474 | RT21045 | ELECTRICAL TECHNOLOGY                    | 9        | 35       | 3       |
| 15A81A04H7 | RT21041 | ELECTRONIC DEVICES AND CIRCUITS          | 7        | 31       | 0       |
| 15A81A04H7 | RT21044 | SIGNALS AND SYSTEMS                      | 9        | 31       | 3       |
| 15A81A0516 | RT21053 | DIGITAL LOGIC DESIGN                     | 18       | -1       | 0       |
| 15A81A0592 | RT21051 | OBJECT ORIENTED PROGRAMMING THROUGH C++  | 20       | 17       | 0       |
| 16A85A0405 | RT21044 | SIGNALS AND SYSTEMS                      | 11       | 32       | 3       |
| 16A85A0407 | RT21044 | SIGNALS AND SYSTEMS                      | 11       | -1       | 0       |
| 16A85A0439 | RT21044 | SIGNALS AND SYSTEMS                      | 8        | 46       | 3       |

\*\*Note:1)[Last Date to apply for Recounting/Revaluation/Challenge Revaluation is : 08-10-2022 ]

\*\*Imp Note:\*\*

\* -1 in the filed of externals indicates student is absent for the respective subject.

\* -2 in the filed of externals indicates student result Withheld for the respective subject.

\* -3 in the filed of externals indicates student involved in Malpractice for the respective subject.

*Signature*

