



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for M.Tech (R16/R13) | Semester Regular /Supplementary Examinations, JANUARY-2018 .

College: VASAVI,TADEPALLIGUDEM:A8

Discrepancy pertaining to these results are to be submitted on or before 02-05-2018 with following documents at CE(PG) Office,JNTUK,Kakinada

Htno	Subcode	Subname	Internal	External	credits
15A81D4304	G4301	ELECTRICAL MACHINE MODELING & ANALYSIS	38	-1	0
15A81D4304	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	36	-1	0
15A81D4304	G5614	MODERN CONTROL THEORY	33	-1	0
15A81D5301	G5602	HVDC TRANSMISSION	22	29	1
15A81D6802	G6801	MICROCONTROLLERS FOR EMBEDDED SYSTEM DES	31	-1	0
15A81D6805	G6801	MICROCONTROLLERS FOR EMBEDDED SYSTEM DES	21	30	1
15A81D6805	G6802	VLSI TECHNOLOGY AND DESIGN	21	27	0
15A81D6805	G6803	CMOS ANALOG IC DESIGN	24	14	0
15A81D6805	G6806	DIGITAL SYSTEM DESIGN	30	10	0
15A81D6805	G6809	CMOS DIGITAL IC DESIGN	24	26	1
15A81D8704	G2201	APPLIED MATHEMATICS	23	10	0
15A81D8707	G2201	APPLIED MATHEMATICS	31	28	1
16A81D1501	I1503	MECHANICAL VIBRATIONS ELECTIVE 1	37	25	1
16A81D4301	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	33	24	1
16A81D4301	I5614	MODERN CONTROL THEORY	36	14	0
16A81D5302	I5614	MODERN CONTROL THEORY	34	32	1
16A81D5305	I5601	MICROPROCESSORS & MICROCONTROLLERS	28	36	1
16A81D5305	I5603	POWER SYSTEM OPERATION AND CONTROL	33	31	1
16A81D5305	I5614	MODERN CONTROL THEORY	31	20	0
16A81D5803	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	32	34	1
16A81D6801	I6801	DIGITAL SYSTEM DESIGN	31	15	0
16A81D6801	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	29	13	0
16A81D6801	I6810	SYSTEM ON CHIP DESIGN ELECTIVE 2	34	26	1
16A81D8701	I2201	ADVANCED MATHEMATICS	31	24	1
16A81D8701	I8703	STRUCTURAL DYNAMICS	23	8	0
17A81D1501	I1501	ADVANCED MECHANICS OF SOLIDS	30	25	1
17A81D1501	I1502	ADVANCED MECHANISMS	37	36	1
17A81D1501	I1503	MECHANICAL VIBRATIONS ELECTIVE 1	36	24	1
17A81D1501	I1505	PRODUCT DESIGN ELECTIVE 1	37	30	1
17A81D1501	I1510	DESIGN FOR MANUFACTURING AND ASSEMBLY EL	38	27	1
17A81D1501	I1512	MACHINE DYNAMICS LAB	35	54	1
17A81D1501	I1801	COMPUTATIONAL METHODS IN ENGINEERING ELE	30	32	1
17A81D1502	I1501	ADVANCED MECHANICS OF SOLIDS	34	35	1
17A81D1502	I1502	ADVANCED MECHANISMS	37	51	1
17A81D1502	I1503	MECHANICAL VIBRATIONS ELECTIVE 1	39	36	1
17A81D1502	I1505	PRODUCT DESIGN ELECTIVE 1	38	35	1
17A81D1502	I1510	DESIGN FOR MANUFACTURING AND ASSEMBLY EL	37	37	1
17A81D1502	I1512	MACHINE DYNAMICS LAB	34	55	1
17A81D1502	I1801	COMPUTATIONAL METHODS IN ENGINEERING ELE	38	42	1
17A81D1503	I1501	ADVANCED MECHANICS OF SOLIDS	32	33	1
17A81D1503	I1502	ADVANCED MECHANISMS	37	45	1

Htno	Subcode	Subname	Internal	External	credits
17A81D1503	I1503	MECHANICAL VIBRATIONS ELECTIVE 1	37	35	1
17A81D1503	I1505	PRODUCT DESIGN ELECTIVE 1	38	37	1
17A81D1503	I1510	DESIGN FOR MANUFACTURING AND ASSEMBLY EL	36	36	1
17A81D1503	I1512	MACHINE DYNAMICS LAB	33	56	1
17A81D1503	I1801	COMPUTATIONAL METHODS IN ENGINEERING ELE	38	39	1
17A81D1504	I1501	ADVANCED MECHANICS OF SOLIDS	13	-1	0
17A81D1504	I1502	ADVANCED MECHANISMS	17	-1	0
17A81D1504	I1503	MECHANICAL VIBRATIONS ELECTIVE 1	15	-1	0
17A81D1504	I1505	PRODUCT DESIGN ELECTIVE 1	13	-1	0
17A81D1504	I1510	DESIGN FOR MANUFACTURING AND ASSEMBLY EL	14	-1	0
17A81D1504	I1512	MACHINE DYNAMICS LAB	20	-1	0
17A81D1504	I1801	COMPUTATIONAL METHODS IN ENGINEERING ELE	16	-1	0
17A81D5301	I5601	MICROPROCESSORS & MICROCONTROLLERS	36	55	1
17A81D5301	I5602	HVDC TRANSMISSION	39	37	1
17A81D5301	I5603	POWER SYSTEM OPERATION AND CONTROL	40	50	1
17A81D5301	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	40	32	1
17A81D5301	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	37	33	1
17A81D5301	I5610	POWER SYSTEM SECURITY ELECTIVE 2	40	36	1
17A81D5301	I5615	SIMULATION LABORATORY	40	58	1
17A81D5302	I5601	MICROPROCESSORS & MICROCONTROLLERS	36	51	1
17A81D5302	I5602	HVDC TRANSMISSION	36	38	1
17A81D5302	I5603	POWER SYSTEM OPERATION AND CONTROL	39	29	1
17A81D5302	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	40	35	1
17A81D5302	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	37	30	1
17A81D5302	I5610	POWER SYSTEM SECURITY ELECTIVE 2	35	33	1
17A81D5302	I5615	SIMULATION LABORATORY	40	59	1
17A81D5303	I5601	MICROPROCESSORS & MICROCONTROLLERS	37	58	1
17A81D5303	I5602	HVDC TRANSMISSION	40	49	1
17A81D5303	I5603	POWER SYSTEM OPERATION AND CONTROL	40	58	1
17A81D5303	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	40	40	1
17A81D5303	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	38	44	1
17A81D5303	I5610	POWER SYSTEM SECURITY ELECTIVE 2	39	34	1
17A81D5303	I5615	SIMULATION LABORATORY	40	59	1
17A81D5304	I5601	MICROPROCESSORS & MICROCONTROLLERS	26	28	1
17A81D5304	I5602	HVDC TRANSMISSION	33	28	1
17A81D5304	I5603	POWER SYSTEM OPERATION AND CONTROL	31	0	0
17A81D5304	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	38	24	1
17A81D5304	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	35	24	1
17A81D5304	I5610	POWER SYSTEM SECURITY ELECTIVE 2	28	13	0
17A81D5304	I5615	SIMULATION LABORATORY	30	28	1
17A81D5305	I5601	MICROPROCESSORS & MICROCONTROLLERS	32	51	1
17A81D5305	I5602	HVDC TRANSMISSION	38	34	1
17A81D5305	I5603	POWER SYSTEM OPERATION AND CONTROL	39	55	1
17A81D5305	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	38	29	1
17A81D5305	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	36	36	1
17A81D5305	I5610	POWER SYSTEM SECURITY ELECTIVE 2	36	31	1
17A81D5305	I5615	SIMULATION LABORATORY	40	55	1
17A81D5306	I5601	MICROPROCESSORS & MICROCONTROLLERS	26	44	1
17A81D5306	I5602	HVDC TRANSMISSION	37	30	1
17A81D5306	I5603	POWER SYSTEM OPERATION AND CONTROL	29	34	1
17A81D5306	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	36	33	1

Htno	Subcode	Subname	Internal	External	credits
17A81D5306	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	33	26	1
17A81D5306	I5610	POWER SYSTEM SECURITY ELECTIVE 2	34	39	1
17A81D5306	I5615	SIMULATION LABORATORY	39	53	1
17A81D5307	I5601	MICROPROCESSORS & MICROCONTROLLERS	28	42	1
17A81D5307	I5602	HVDC TRANSMISSION	34	42	1
17A81D5307	I5603	POWER SYSTEM OPERATION AND CONTROL	32	37	1
17A81D5307	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	38	36	1
17A81D5307	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	36	24	1
17A81D5307	I5610	POWER SYSTEM SECURITY ELECTIVE 2	35	27	1
17A81D5307	I5615	SIMULATION LABORATORY	39	50	1
17A81D5308	I5601	MICROPROCESSORS & MICROCONTROLLERS	36	50	1
17A81D5308	I5602	HVDC TRANSMISSION	35	38	1
17A81D5308	I5603	POWER SYSTEM OPERATION AND CONTROL	36	27	1
17A81D5308	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	39	33	1
17A81D5308	I5605	ELECTRICAL DISTRIBUTION SYSTEMS ELECTIVE	37	28	1
17A81D5308	I5610	POWER SYSTEM SECURITY ELECTIVE 2	39	20	0
17A81D5308	I5615	SIMULATION LABORATORY	40	58	1
17A81D5801	I0504	ADVANCED OPERATING SYSTEM	36	33	1
17A81D5801	I0505	DATA WAREHOUSING AND DATA MINING	31	28	1
17A81D5801	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	31	24	1
17A81D5801	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	39	30	1
17A81D5801	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	27	26	1
17A81D5801	I5803	DATABASE MANAGEMENT SYSTEMS	37	36	1
17A81D5801	I5805	CSE LAB 1	37	57	1
17A81D5803	I0504	ADVANCED OPERATING SYSTEM	34	33	1
17A81D5803	I0505	DATA WAREHOUSING AND DATA MINING	33	30	1
17A81D5803	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	36	26	1
17A81D5803	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	38	28	1
17A81D5803	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	30	27	1
17A81D5803	I5803	DATABASE MANAGEMENT SYSTEMS	39	33	1
17A81D5803	I5805	CSE LAB 1	38	58	1
17A81D5804	I0504	ADVANCED OPERATING SYSTEM	34	27	1
17A81D5804	I0505	DATA WAREHOUSING AND DATA MINING	29	24	1
17A81D5804	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	31	5	0
17A81D5804	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	36	24	1
17A81D5804	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	24	26	1
17A81D5804	I5803	DATABASE MANAGEMENT SYSTEMS	39	27	1
17A81D5804	I5805	CSE LAB 1	37	58	1
17A81D5805	I0504	ADVANCED OPERATING SYSTEM	33	33	1
17A81D5805	I0505	DATA WAREHOUSING AND DATA MINING	34	27	1
17A81D5805	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	36	24	1
17A81D5805	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	38	24	1
17A81D5805	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	28	24	1
17A81D5805	I5803	DATABASE MANAGEMENT SYSTEMS	37	30	1
17A81D5805	I5805	CSE LAB 1	38	57	1
17A81D6801	I6801	DIGITAL SYSTEM DESIGN	35	33	1
17A81D6801	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	39	35	1
17A81D6801	I6803	CMOS ANALOG IC DESIGN	39	49	1
17A81D6801	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	38	38	1
17A81D6801	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	35	24	1
17A81D6801	I6810	SYSTEM ON CHIP DESIGN ELECTIVE 2	36	37	1

Htno	Subcode	Subname	Internal	External	credits
17A81D6801	I6812	VLSI LABORATORY	39	55	1
17A81D6802	I6801	DIGITAL SYSTEM DESIGN	36	32	1
17A81D6802	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	38	32	1
17A81D6802	I6803	CMOS ANALOG IC DESIGN	40	42	1
17A81D6802	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	38	35	1
17A81D6802	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	34	24	1
17A81D6802	I6810	SYSTEM ON CHIP DESIGN ELECTIVE 2	35	28	1
17A81D6802	I6812	VLSI LABORATORY	39	59	1
17A81D6803	I6801	DIGITAL SYSTEM DESIGN	35	26	1
17A81D6803	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	38	33	1
17A81D6803	I6803	CMOS ANALOG IC DESIGN	39	43	1
17A81D6803	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	38	34	1
17A81D6803	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	37	19	0
17A81D6803	I6810	SYSTEM ON CHIP DESIGN ELECTIVE 2	35	29	1
17A81D6803	I6812	VLSI LABORATORY	38	58	1
17A81D6804	I6801	DIGITAL SYSTEM DESIGN	36	26	1
17A81D6804	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	37	24	1
17A81D6804	I6803	CMOS ANALOG IC DESIGN	40	40	1
17A81D6804	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	38	31	1
17A81D6804	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	33	25	1
17A81D6804	I6810	SYSTEM ON CHIP DESIGN ELECTIVE 2	35	28	1
17A81D6804	I6812	VLSI LABORATORY	38	56	1
17A81D6805	I6801	DIGITAL SYSTEM DESIGN	35	28	1
17A81D6805	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	39	35	1
17A81D6805	I6803	CMOS ANALOG IC DESIGN	38	48	1
17A81D6805	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	37	24	1
17A81D6805	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	31	27	1
17A81D6805	I6810	SYSTEM ON CHIP DESIGN ELECTIVE 2	34	29	1
17A81D6805	I6812	VLSI LABORATORY	38	56	1
17A81D8701	I2201	ADVANCED MATHEMATICS	36	24	1
17A81D8701	I8701	THEORY OF ELASTICITY	29	32	1
17A81D8701	I8702	MATRIX ANALYSIS OF STRUCTURES	32	14	0
17A81D8701	I8703	STRUCTURAL DYNAMICS	28	10	0
17A81D8701	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	32	27	1
17A81D8701	I8707	REPAIR AND REHABILITATION OF STRUCTURES	23	24	0
17A81D8701	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	38	47	1
17A81D8702	I2201	ADVANCED MATHEMATICS	29	26	1
17A81D8702	I8701	THEORY OF ELASTICITY	12	30	0
17A81D8702	I8702	MATRIX ANALYSIS OF STRUCTURES	15	12	0
17A81D8702	I8703	STRUCTURAL DYNAMICS	11	9	0
17A81D8702	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	17	24	0
17A81D8702	I8707	REPAIR AND REHABILITATION OF STRUCTURES	17	19	0
17A81D8702	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	32	44	1
17A81D8703	I2201	ADVANCED MATHEMATICS	31	24	1
17A81D8703	I8701	THEORY OF ELASTICITY	21	33	1
17A81D8703	I8702	MATRIX ANALYSIS OF STRUCTURES	26	24	1
17A81D8703	I8703	STRUCTURAL DYNAMICS	21	29	1
17A81D8703	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	28	25	1
17A81D8703	I8707	REPAIR AND REHABILITATION OF STRUCTURES	22	27	0
17A81D8703	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	35	45	1
17A81D8704	I2201	ADVANCED MATHEMATICS	37	32	1

Htno	Subcode	Subname	Internal	External	credits
17A81D8704	I8701	THEORY OF ELASTICITY	31	40	1
17A81D8704	I8702	MATRIX ANALYSIS OF STRUCTURES	32	26	1
17A81D8704	I8703	STRUCTURAL DYNAMICS	23	31	1
17A81D8704	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	36	35	1
17A81D8704	I8707	REPAIR AND REHABILITATION OF STRUCTURES	36	37	1
17A81D8704	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	37	46	1
17A81D8705	I2201	ADVANCED MATHEMATICS	36	25	1
17A81D8705	I8701	THEORY OF ELASTICITY	23	35	1
17A81D8705	I8702	MATRIX ANALYSIS OF STRUCTURES	29	25	1
17A81D8705	I8703	STRUCTURAL DYNAMICS	21	31	1
17A81D8705	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	33	30	1
17A81D8705	I8707	REPAIR AND REHABILITATION OF STRUCTURES	35	30	1
17A81D8705	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	34	43	1
17A81D8706	I2201	ADVANCED MATHEMATICS	38	35	1
17A81D8706	I8701	THEORY OF ELASTICITY	36	36	1
17A81D8706	I8702	MATRIX ANALYSIS OF STRUCTURES	38	26	1
17A81D8706	I8703	STRUCTURAL DYNAMICS	31	38	1
17A81D8706	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	33	33	1
17A81D8706	I8707	REPAIR AND REHABILITATION OF STRUCTURES	34	28	1
17A81D8706	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	38	49	1
17A81D8707	I2201	ADVANCED MATHEMATICS	33	28	1
17A81D8707	I8701	THEORY OF ELASTICITY	24	31	1
17A81D8707	I8702	MATRIX ANALYSIS OF STRUCTURES	29	16	0
17A81D8707	I8703	STRUCTURAL DYNAMICS	20	10	0
17A81D8707	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	25	20	0
17A81D8707	I8707	REPAIR AND REHABILITATION OF STRUCTURES	19	14	0
17A81D8707	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	30	40	1
17A81D8708	I2201	ADVANCED MATHEMATICS	39	40	1
17A81D8708	I8701	THEORY OF ELASTICITY	37	45	1
17A81D8708	I8702	MATRIX ANALYSIS OF STRUCTURES	40	36	1
17A81D8708	I8703	STRUCTURAL DYNAMICS	33	40	1
17A81D8708	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	39	37	1
17A81D8708	I8707	REPAIR AND REHABILITATION OF STRUCTURES	39	41	1
17A81D8708	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	38	50	1
17A81D8709	I2201	ADVANCED MATHEMATICS	30	24	1
17A81D8709	I8701	THEORY OF ELASTICITY	23	28	1
17A81D8709	I8702	MATRIX ANALYSIS OF STRUCTURES	26	13	0
17A81D8709	I8703	STRUCTURAL DYNAMICS	22	28	1
17A81D8709	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	25	29	1
17A81D8709	I8707	REPAIR AND REHABILITATION OF STRUCTURES	23	25	0
17A81D8709	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	34	43	1
17A81D8710	I2201	ADVANCED MATHEMATICS	30	27	1
17A81D8710	I8701	THEORY OF ELASTICITY	20	38	1
17A81D8710	I8702	MATRIX ANALYSIS OF STRUCTURES	23	13	0
17A81D8710	I8703	STRUCTURAL DYNAMICS	21	29	1
17A81D8710	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	30	27	1
17A81D8710	I8707	REPAIR AND REHABILITATION OF STRUCTURES	36	18	0
17A81D8710	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	34	42	1
17A81D8711	I2201	ADVANCED MATHEMATICS	38	32	1
17A81D8711	I8701	THEORY OF ELASTICITY	37	39	1
17A81D8711	I8702	MATRIX ANALYSIS OF STRUCTURES	36	29	1

Htno	Subcode	Subname	Internal	External	credits
17A81D8711	I8703	STRUCTURAL DYNAMICS	31	34	1
17A81D8711	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	36	34	1
17A81D8711	I8707	REPAIR AND REHABILITATION OF STRUCTURES	39	24	1
17A81D8711	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	38	50	1

**\*\*Note:1)**For Recounting/Revaluation/Challenge By Revaluation Apply through Online([www.jntukresults.edu.in](http://www.jntukresults.edu.in))

**\*\*NOTE:2** [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: **09-05-2018**]

**\*\*NOTE:3** [Please inform to the students to enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation ]

**\*\*NOTE:**

[-1 in the filed of externals indicates student absent for the respective subject.

-2 in the filed of externals indicates student result is withheld for the respective subject.

-3 in the filed of externals indicates Malpractice for the respective subject. ]

Date:26-04-2018

*N. Mohan Rao*  
Controller of Examinations