



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for M.Tech II SEMESTER (R16 / R13) Regular / Supplementary Examinations , JUNE- 2018 .

College: VASAVI,TADEPALLIGUDEM:A8

Discrepancy pertaining to these results are to be submitted on or before 24-09-2018 with following documents at CE(PG) Office,JNTUK,Kakinada

Htno	Subcode	Subname	Internal	External	credits
15A81D5301	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	33	-1	0
15A81D5304	H5601	POWER SYSTEM DYNAMICS AND STABILITY	30	-1	0
15A81D5304	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	20	-1	0
15A81D5304	H5604	ADVANCED POWER SYSTEM PROTECTION	30	-1	0
15A81D5304	H5605	SMART GRID	35	-1	0
15A81D5304	H5610	HIGH VOLTAGE TESTING TECHNIQUES	15	-1	0
15A81D5805	H5801	COMPUTER NETWORKS	21	30	1
15A81D8704	H8710	EARTH RETAINING STRUCTURES ELECTIVE-IV	26	32	1
17A81D1501	J1501	OPTIMIZATION AND RELIABILITY	36	36	1
17A81D1501	J1502	EXPERIMENTAL STRESS ANALYSIS	37	25	1
17A81D1501	J1503	DESIGN WITH ADVANCED MATERIALS	33	27	1
17A81D1501	J1504	TRIBOLOGY ELECTIVE-III	38	28	1
17A81D1501	J1509	MECHATRONICS ELECTIVE-IV	39	28	1
17A81D1501	J1511	DESIGN PRACTICE LAB	35	40	1
17A81D1501	J2103	FINITE ELEMENT METHODS	38	25	1
17A81D1502	J1501	OPTIMIZATION AND RELIABILITY	38	50	1
17A81D1502	J1502	EXPERIMENTAL STRESS ANALYSIS	39	34	1
17A81D1502	J1503	DESIGN WITH ADVANCED MATERIALS	36	37	1
17A81D1502	J1504	TRIBOLOGY ELECTIVE-III	38	45	1
17A81D1502	J1509	MECHATRONICS ELECTIVE-IV	39	32	1
17A81D1502	J1511	DESIGN PRACTICE LAB	34	40	1
17A81D1502	J2103	FINITE ELEMENT METHODS	38	39	1
17A81D1503	J1501	OPTIMIZATION AND RELIABILITY	38	48	1
17A81D1503	J1502	EXPERIMENTAL STRESS ANALYSIS	39	36	1
17A81D1503	J1503	DESIGN WITH ADVANCED MATERIALS	35	42	1
17A81D1503	J1504	TRIBOLOGY ELECTIVE-III	38	37	1
17A81D1503	J1509	MECHATRONICS ELECTIVE-IV	39	31	1
17A81D1503	J1511	DESIGN PRACTICE LAB	35	45	1
17A81D1503	J2103	FINITE ELEMENT METHODS	38	31	1
17A81D5301	J5601	POWER SYSTEM DYNAMICS AND STABILITY	40	36	1
17A81D5301	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIV	38	40	1
17A81D5301	J5603	REAL TIME CONTROL OF POWER SYSTEMS	39	35	1
17A81D5301	J5604	ADVANCED POWER SYSTEM PROTECTION	39	29	1
17A81D5301	J5606	POWER QUALITY ELECTIVEIII	36	48	1
17A81D5301	J5610	HIGH VOLTAGE TESTING TECHNIQUES	39	39	1
17A81D5301	J5613	POWER SYSTEMS LABORATORY	40	55	1
17A81D5302	J5601	POWER SYSTEM DYNAMICS AND STABILITY	40	48	1
17A81D5302	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIV	40	31	1
17A81D5302	J5603	REAL TIME CONTROL OF POWER SYSTEMS	38	34	1
17A81D5302	J5604	ADVANCED POWER SYSTEM PROTECTION	39	35	1
17A81D5302	J5606	POWER QUALITY ELECTIVEIII	39	40	1

Htno	Subcode	Subname	Internal	External	credits
17A81D5302	J5610	HIGH VOLTAGE TESTING TECHNIQUES	38	42	1
17A81D5302	J5613	POWER SYSTEMS LABORATORY	40	58	1
17A81D5303	J5601	POWER SYSTEM DYNAMICS AND STABILITY	40	49	1
17A81D5303	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIV	40	36	1
17A81D5303	J5603	REAL TIME CONTROL OF POWER SYSTEMS	39	42	1
17A81D5303	J5604	ADVANCED POWER SYSTEM PROTECTION	40	44	1
17A81D5303	J5606	POWER QUALITY ELECTIVEIII	40	51	1
17A81D5303	J5610	HIGH VOLTAGE TESTING TECHNIQUES	39	40	1
17A81D5303	J5613	POWER SYSTEMS LABORATORY	40	59	1
17A81D5305	J5601	POWER SYSTEM DYNAMICS AND STABILITY	40	30	1
17A81D5305	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIV	33	31	1
17A81D5305	J5603	REAL TIME CONTROL OF POWER SYSTEMS	39	29	1
17A81D5305	J5604	ADVANCED POWER SYSTEM PROTECTION	37	27	1
17A81D5305	J5606	POWER QUALITY ELECTIVEIII	36	43	1
17A81D5305	J5610	HIGH VOLTAGE TESTING TECHNIQUES	38	35	1
17A81D5305	J5613	POWER SYSTEMS LABORATORY	40	53	1
17A81D5306	J5601	POWER SYSTEM DYNAMICS AND STABILITY	39	26	1
17A81D5306	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIV	38	26	1
17A81D5306	J5603	REAL TIME CONTROL OF POWER SYSTEMS	38	25	1
17A81D5306	J5604	ADVANCED POWER SYSTEM PROTECTION	35	14	0
17A81D5306	J5606	POWER QUALITY ELECTIVEIII	34	53	1
17A81D5306	J5610	HIGH VOLTAGE TESTING TECHNIQUES	37	33	1
17A81D5306	J5613	POWER SYSTEMS LABORATORY	40	55	1
17A81D5307	J5601	POWER SYSTEM DYNAMICS AND STABILITY	38	32	1
17A81D5307	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIV	39	30	1
17A81D5307	J5603	REAL TIME CONTROL OF POWER SYSTEMS	39	32	1
17A81D5307	J5604	ADVANCED POWER SYSTEM PROTECTION	36	24	1
17A81D5307	J5606	POWER QUALITY ELECTIVEIII	35	38	1
17A81D5307	J5610	HIGH VOLTAGE TESTING TECHNIQUES	37	25	1
17A81D5307	J5613	POWER SYSTEMS LABORATORY	40	54	1
17A81D5308	J5601	POWER SYSTEM DYNAMICS AND STABILITY	40	-1	0
17A81D5308	J5602	FLEXIBLE AC TRANSMISSION SYSTEMS ELECTIV	39	-1	0
17A81D5308	J5603	REAL TIME CONTROL OF POWER SYSTEMS	39	-1	0
17A81D5308	J5604	ADVANCED POWER SYSTEM PROTECTION	37	-1	0
17A81D5308	J5606	POWER QUALITY ELECTIVEIII	35	-1	0
17A81D5308	J5610	HIGH VOLTAGE TESTING TECHNIQUES	38	-1	0
17A81D5308	J5613	POWER SYSTEMS LABORATORY	40	55	1
17A81D5801	J0502	SOFTWARE ENGINEERING ELECTIVE I	36	34	1
17A81D5801	J2503	CYBER SECURITY	38	24	1
17A81D5801	J2510	CLOUD COMPUTING ELECTIVE II	35	29	1
17A81D5801	J4001	ADVANCED UNIX PROGRAMMING	35	36	1
17A81D5801	J4002	BIG DATA ANALYTICS	32	32	1
17A81D5801	J5801	COMPUTER NETWORKS	37	39	1
17A81D5801	J5803	CSE LAB 2	38	56	1
17A81D5803	J0502	SOFTWARE ENGINEERING ELECTIVE I	37	35	1
17A81D5803	J2503	CYBER SECURITY	38	24	1
17A81D5803	J2510	CLOUD COMPUTING ELECTIVE II	37	28	1
17A81D5803	J4001	ADVANCED UNIX PROGRAMMING	35	43	1
17A81D5803	J4002	BIG DATA ANALYTICS	32	29	1
17A81D5803	J5801	COMPUTER NETWORKS	39	41	1
17A81D5803	J5803	CSE LAB 2	38	57	1

Htno	Subcode	Subname	Internal	External	credits
17A81D5804	J0502	SOFTWARE ENGINEERING ELECTIVE I	20	29	0
17A81D5804	J2503	CYBER SECURITY	20	15	0
17A81D5804	J2510	CLOUD COMPUTING ELECTIVE II	18	17	0
17A81D5804	J4001	ADVANCED UNIX PROGRAMMING	15	30	0
17A81D5804	J4002	BIG DATA ANALYTICS	2	27	0
17A81D5804	J5801	COMPUTER NETWORKS	18	28	0
17A81D5804	J5803	CSE LAB 2	37	52	1
17A81D5805	J0502	SOFTWARE ENGINEERING ELECTIVE I	36	27	1
17A81D5805	J2503	CYBER SECURITY	38	28	1
17A81D5805	J2510	CLOUD COMPUTING ELECTIVE II	36	45	1
17A81D5805	J4001	ADVANCED UNIX PROGRAMMING	34	34	1
17A81D5805	J4002	BIG DATA ANALYTICS	33	37	1
17A81D5805	J5801	COMPUTER NETWORKS	39	40	1
17A81D5805	J5803	CSE LAB 2	38	56	1
17A81D6801	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	39	42	1
17A81D6801	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	38	50	1
17A81D6801	J6803	EMBEDDED REAL TIME OPERATING SYSTEMS EL	38	44	1
17A81D6801	J6804	DESIGN FOR TESTABILITY	39	48	1
17A81D6801	J6805	DSP PROCESSORS AND ARCHITECTURES	39	50	1
17A81D6801	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	38	50	1
17A81D6801	J6811	EMBEDDED SYSTEM DESIGN LABORATORY	37	57	1
17A81D6802	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	38	39	1
17A81D6802	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	36	50	1
17A81D6802	J6803	EMBEDDED REAL TIME OPERATING SYSTEMS EL	37	49	1
17A81D6802	J6804	DESIGN FOR TESTABILITY	38	41	1
17A81D6802	J6805	DSP PROCESSORS AND ARCHITECTURES	39	48	1
17A81D6802	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	39	38	1
17A81D6802	J6811	EMBEDDED SYSTEM DESIGN LABORATORY	37	58	1
17A81D6803	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	38	32	1
17A81D6803	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	38	50	1
17A81D6803	J6803	EMBEDDED REAL TIME OPERATING SYSTEMS EL	36	34	1
17A81D6803	J6804	DESIGN FOR TESTABILITY	38	39	1
17A81D6803	J6805	DSP PROCESSORS AND ARCHITECTURES	39	43	1
17A81D6803	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	38	30	1
17A81D6803	J6811	EMBEDDED SYSTEM DESIGN LABORATORY	37	58	1
17A81D6804	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	39	14	0
17A81D6804	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	37	49	1
17A81D6804	J6803	EMBEDDED REAL TIME OPERATING SYSTEMS EL	35	41	1
17A81D6804	J6804	DESIGN FOR TESTABILITY	39	33	1
17A81D6804	J6805	DSP PROCESSORS AND ARCHITECTURES	39	44	1
17A81D6804	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	37	45	1
17A81D6804	J6811	EMBEDDED SYSTEM DESIGN LABORATORY	37	56	1
17A81D6805	J6801	EMBEDDED SYSTEM DESIGN ELECTIVEIII	39	39	1
17A81D6805	J6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	36	54	1
17A81D6805	J6803	EMBEDDED REAL TIME OPERATING SYSTEMS EL	38	47	1
17A81D6805	J6804	DESIGN FOR TESTABILITY	39	47	1
17A81D6805	J6805	DSP PROCESSORS AND ARCHITECTURES	39	43	1
17A81D6805	J6809	CPLD AND FPGA ARCHITECTURES AND APPLICAT	38	50	1
17A81D6805	J6811	EMBEDDED SYSTEM DESIGN LABORATORY	39	58	1
17A81D8701	J8701	FINITE ELEMENT METHODS	31	14	0
17A81D8701	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	21	17	0

Htno	Subcode	Subname	Internal	External	credits
17A81D8701	J8703	STABILITY OF STRUCTURES	36	24	1
17A81D8701	J8704	THEORY OF PLATES & SHELLS	32	24	1
17A81D8701	J8705	PRESTRESSED CONCRETE ELECTIVEI	27	30	1
17A81D8701	J8710	EARTH RETAINING STRUCTURES ELECTIVEII	31	35	1
17A81D8701	J8711	CAD LABORATORY	34	52	1
17A81D8702	J8701	FINITE ELEMENT METHODS	21	11	0
17A81D8702	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	10	12	0
17A81D8702	J8703	STABILITY OF STRUCTURES	12	13	0
17A81D8702	J8704	THEORY OF PLATES & SHELLS	17	20	0
17A81D8702	J8705	PRESTRESSED CONCRETE ELECTIVEI	19	16	0
17A81D8702	J8710	EARTH RETAINING STRUCTURES ELECTIVEII	26	24	1
17A81D8702	J8711	CAD LABORATORY	34	53	1
17A81D8703	J8701	FINITE ELEMENT METHODS	27	39	1
17A81D8703	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	18	32	1
17A81D8703	J8703	STABILITY OF STRUCTURES	35	13	0
17A81D8703	J8704	THEORY OF PLATES & SHELLS	31	28	1
17A81D8703	J8705	PRESTRESSED CONCRETE ELECTIVEI	22	24	0
17A81D8703	J8710	EARTH RETAINING STRUCTURES ELECTIVEII	33	37	1
17A81D8703	J8711	CAD LABORATORY	33	52	1
17A81D8704	J8701	FINITE ELEMENT METHODS	33	31	1
17A81D8704	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	28	31	1
17A81D8704	J8703	STABILITY OF STRUCTURES	39	32	1
17A81D8704	J8704	THEORY OF PLATES & SHELLS	33	30	1
17A81D8704	J8705	PRESTRESSED CONCRETE ELECTIVEI	33	33	1
17A81D8704	J8710	EARTH RETAINING STRUCTURES ELECTIVEII	37	45	1
17A81D8704	J8711	CAD LABORATORY	34	55	1
17A81D8705	J8701	FINITE ELEMENT METHODS	28	-1	0
17A81D8705	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	25	32	1
17A81D8705	J8703	STABILITY OF STRUCTURES	29	30	1
17A81D8705	J8704	THEORY OF PLATES & SHELLS	29	29	1
17A81D8705	J8705	PRESTRESSED CONCRETE ELECTIVEI	28	-1	0
17A81D8705	J8710	EARTH RETAINING STRUCTURES ELECTIVEII	36	25	1
17A81D8705	J8711	CAD LABORATORY	33	51	1
17A81D8706	J8701	FINITE ELEMENT METHODS	38	52	1
17A81D8706	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	30	26	1
17A81D8706	J8703	STABILITY OF STRUCTURES	36	30	1
17A81D8706	J8704	THEORY OF PLATES & SHELLS	36	30	1
17A81D8706	J8705	PRESTRESSED CONCRETE ELECTIVEI	35	34	1
17A81D8706	J8710	EARTH RETAINING STRUCTURES ELECTIVEII	37	40	1
17A81D8706	J8711	CAD LABORATORY	37	55	1
17A81D8707	J8701	FINITE ELEMENT METHODS	22	0	0
17A81D8707	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	19	0	0
17A81D8707	J8703	STABILITY OF STRUCTURES	14	8	0
17A81D8707	J8704	THEORY OF PLATES & SHELLS	19	10	0
17A81D8707	J8705	PRESTRESSED CONCRETE ELECTIVEI	12	7	0
17A81D8707	J8710	EARTH RETAINING STRUCTURES ELECTIVEII	25	7	0
17A81D8707	J8711	CAD LABORATORY	31	50	1
17A81D8708	J8701	FINITE ELEMENT METHODS	39	58	1
17A81D8708	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	31	37	1
17A81D8708	J8703	STABILITY OF STRUCTURES	40	27	1
17A81D8708	J8704	THEORY OF PLATES & SHELLS	38	33	1

Htno	Subcode	Subname	Internal	External	credits
17A81D8708	J8705	PRESTRESSED CONCRETE ELECTIVEI	34	43	1
17A81D8708	J8710	EARTH RETAINING STRUCTURES ELECTIVEII	39	48	1
17A81D8708	J8711	CAD LABORATORY	37	55	1
17A81D8709	J8701	FINITE ELEMENT METHODS	23	31	1
17A81D8709	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	19	25	0
17A81D8709	J8703	STABILITY OF STRUCTURES	22	13	0
17A81D8709	J8704	THEORY OF PLATES & SHELLS	23	19	0
17A81D8709	J8705	PRESTRESSED CONCRETE ELECTIVEI	13	16	0
17A81D8709	J8710	EARTH RETAINING STRUCTURES ELECTIVEII	24	27	1
17A81D8709	J8711	CAD LABORATORY	32	50	1
17A81D8710	J8701	FINITE ELEMENT METHODS	33	27	1
17A81D8710	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	18	25	0
17A81D8710	J8703	STABILITY OF STRUCTURES	38	33	1
17A81D8710	J8704	THEORY OF PLATES & SHELLS	26	26	1
17A81D8710	J8705	PRESTRESSED CONCRETE ELECTIVEI	23	27	1
17A81D8710	J8710	EARTH RETAINING STRUCTURES ELECTIVEII	32	31	1
17A81D8710	J8711	CAD LABORATORY	34	52	1
17A81D8711	J8701	FINITE ELEMENT METHODS	39	35	1
17A81D8711	J8702	EARTHQUAKE RESISTANT STRUCTURES ELECTIVE	35	35	1
17A81D8711	J8703	STABILITY OF STRUCTURES	39	26	1
17A81D8711	J8704	THEORY OF PLATES & SHELLS	39	28	1
17A81D8711	J8705	PRESTRESSED CONCRETE ELECTIVEI	35	25	1
17A81D8711	J8710	EARTH RETAINING STRUCTURES ELECTIVEII	38	39	1
17A81D8711	J8711	CAD LABORATORY	37	55	1

**Note:1)For Recounting/Revaluation/Challenge By Revaluation Apply through Online(www.jntukresults.edu.in)

NOTE:2 [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: **01-10-2018]

**NOTE:3 [Please inform to the students to enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation]

**NOTE:

[-1 in the filed of externals indicates student absent for the respective subject.

-2 in the filed of externals indicates student result is withheld for the respective subject.

-3 in the filed of externals indicates Malpractice for the respective subject.]

Date:17-09-2018

N. Mohan Rao
Controller of Examinations