



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for M.Tech (R13/R09)II SEMESTER REG/SUPPLE EXAMINATIONS Oct-2015

College name: VASAVI,TADEPALLIGUDEM:A8

Discrepancy pertaining to this results supposed to be cleared on or before 18-02-2016 with following documents at CE Office,JNTUK,Kakinada

- Online Registration Proof
- Hallticket
- DForm(Online)
- DForm(Offline)
- Attendance Sheet
- Any Other supporting Documents

Htno	Subcode	Subname	Internal	External	credits
13A81D3801	H0602	CMOS ANALOG AND DIGITAL IC DESIGN	26	-1	0
13A81D3801	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	25	28	1
13A81D3804	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	23	34	1
13A81D3805	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	22	25	0
13A81D3809	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	26	25	1
13A81D5809	H0501	DATA WAREHOUSING AND DATA MINING	34	43	1
13A81D5809	H0507	HUMAN COMPUTER INTERACTION	34	39	1
13A81D5809	H2508	CLOUD COMPUTING	34	40	1
13A81D5809	H4002	INFORMATION SECURITY	29	42	1
13A81D5809	H5801	COMPUTER NETWORKS	37	38	1
13A81D5809	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	30	31	1
13A81D5809	H5806	CSE LAB-2	35	50	1
13A81D6804	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	21	37	1
13A81D6811	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	29	27	1
14A81D1501	H1501	OPTIMIZATION AND RELIABILITY	38	32	1
14A81D1501	H1502	EXPERIMENTAL STRESS ANALYSIS	34	45	1
14A81D1501	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	34	41	1
14A81D1501	H1507	TRIBOLOGY ELECTIVE-III	27	37	1
14A81D1501	H1511	MECHANICS OF COMPOSITE MATERIALS ELECTIV	34	31	1
14A81D1501	H1514	DESIGN PRACTICE LAB	35	56	1
14A81D1501	H2103	FINITE ELEMENT METHOD	28	28	1
14A81D1502	H1501	OPTIMIZATION AND RELIABILITY	39	41	1
14A81D1502	H1502	EXPERIMENTAL STRESS ANALYSIS	39	30	1
14A81D1502	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	37	36	1
14A81D1502	H1507	TRIBOLOGY ELECTIVE-III	31	36	1
14A81D1502	H1511	MECHANICS OF COMPOSITE MATERIALS ELECTIV	36	24	1
14A81D1502	H1514	DESIGN PRACTICE LAB	39	58	1
14A81D1502	H2103	FINITE ELEMENT METHOD	34	16	0
14A81D1503	H1501	OPTIMIZATION AND RELIABILITY	39	32	1
14A81D1503	H1502	EXPERIMENTAL STRESS ANALYSIS	39	35	1
14A81D1503	H1505	DESIGN FOR MANUFACTURING ELECTIVE-II	35	40	1
14A81D1503	H1507	TRIBOLOGY ELECTIVE-III	26	36	1
14A81D1503	H1511	MECHANICS OF COMPOSITE MATERIALS ELECTIV	34	28	1
14A81D1503	H1514	DESIGN PRACTICE LAB	37	58	1

Htno	Subcode	Subname	Internal	External	credits
14A81D1503	H2103	FINITE ELEMENT METHOD	34	44	1
14A81D3801	H0602	CMOS ANALOG AND DIGITAL IC DESIGN	28	40	1
14A81D3801	H3801	CODING THEORY & APPLICATIONS	31	32	1
14A81D3801	H3803	ADVANCED COMMUNICATIONS LAB	38	57	1
14A81D3801	H4502	IMAGE & VIDEO PROCESSING	29	35	1
14A81D3801	H4503	WIRELESS COMMUNICATION & NETWORKS	38	41	1
14A81D3801	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	32	33	1
14A81D3801	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	31	38	1
14A81D3802	H0602	CMOS ANALOG AND DIGITAL IC DESIGN	24	34	1
14A81D3802	H3801	CODING THEORY & APPLICATIONS	26	29	1
14A81D3802	H3803	ADVANCED COMMUNICATIONS LAB	36	56	1
14A81D3802	H4502	IMAGE & VIDEO PROCESSING	29	29	1
14A81D3802	H4503	WIRELESS COMMUNICATION & NETWORKS	32	25	1
14A81D3802	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	26	27	1
14A81D3802	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	33	24	1
14A81D3803	H0602	CMOS ANALOG AND DIGITAL IC DESIGN	26	38	1
14A81D3803	H3801	CODING THEORY & APPLICATIONS	27	28	1
14A81D3803	H3803	ADVANCED COMMUNICATIONS LAB	38	58	1
14A81D3803	H4502	IMAGE & VIDEO PROCESSING	29	31	1
14A81D3803	H4503	WIRELESS COMMUNICATION & NETWORKS	37	37	1
14A81D3803	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	29	42	1
14A81D3803	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	33	31	1
14A81D3804	H0602	CMOS ANALOG AND DIGITAL IC DESIGN	26	34	1
14A81D3804	H3801	CODING THEORY & APPLICATIONS	26	31	1
14A81D3804	H3803	ADVANCED COMMUNICATIONS LAB	37	56	1
14A81D3804	H4502	IMAGE & VIDEO PROCESSING	29	36	1
14A81D3804	H4503	WIRELESS COMMUNICATION & NETWORKS	34	34	1
14A81D3804	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	29	39	1
14A81D3804	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	27	26	1
14A81D3805	H0602	CMOS ANALOG AND DIGITAL IC DESIGN	24	9	0
14A81D3805	H3801	CODING THEORY & APPLICATIONS	26	24	1
14A81D3805	H3803	ADVANCED COMMUNICATIONS LAB	35	46	1
14A81D3805	H4502	IMAGE & VIDEO PROCESSING	29	26	1
14A81D3805	H4503	WIRELESS COMMUNICATION & NETWORKS	34	13	0
14A81D3805	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	24	26	1
14A81D3805	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	27	6	0
14A81D4301	H4301	SWITCHED MODE POWER CONVERSION	40	30	1
14A81D4301	H4302	ELECTRIC DRIVES-II	39	49	1
14A81D4301	H4303	DIGITAL CONTROLLERS	36	32	1
14A81D4301	H4304	CUSTOM POWER DEVICES	37	30	1
14A81D4301	H4305	RENEWABLE ENERGY SYSTEMS ELECTIVE-III	39	31	1
14A81D4301	H4310	POWER CONVERTERS & DRIVES LAB	40	60	1
14A81D4301	H5605	SMART GRID	38	35	1
14A81D4302	H4301	SWITCHED MODE POWER CONVERSION	37	24	1
14A81D4302	H4302	ELECTRIC DRIVES-II	38	45	1
14A81D4302	H4303	DIGITAL CONTROLLERS	30	30	1
14A81D4302	H4304	CUSTOM POWER DEVICES	37	34	1
14A81D4302	H4305	RENEWABLE ENERGY SYSTEMS ELECTIVE-III	35	36	1
14A81D4302	H4310	POWER CONVERTERS & DRIVES LAB	39	59	1
14A81D4302	H5605	SMART GRID	37	29	1
14A81D4303	H4301	SWITCHED MODE POWER CONVERSION	38	24	1

Htno	Subcode	Subname	Internal	External	credits
14A81D4303	H4302	ELECTRIC DRIVES-II	39	47	1
14A81D4303	H4303	DIGITAL CONTROLLERS	33	34	1
14A81D4303	H4304	CUSTOM POWER DEVICES	38	39	1
14A81D4303	H4305	RENEWABLE ENERGY SYSTEMS ELECTIVE-III	39	33	1
14A81D4303	H4310	POWER CONVERTERS & DRIVES LAB	39	59	1
14A81D4303	H5605	SMART GRID	40	33	1
14A81D4304	H4301	SWITCHED MODE POWER CONVERSION	38	24	1
14A81D4304	H4302	ELECTRIC DRIVES-II	36	44	1
14A81D4304	H4303	DIGITAL CONTROLLERS	32	29	1
14A81D4304	H4304	CUSTOM POWER DEVICES	38	26	1
14A81D4304	H4305	RENEWABLE ENERGY SYSTEMS ELECTIVE-III	35	27	1
14A81D4304	H4310	POWER CONVERTERS & DRIVES LAB	38	58	1
14A81D4304	H5605	SMART GRID	36	36	1
14A81D4305	H4301	SWITCHED MODE POWER CONVERSION	39	27	1
14A81D4305	H4302	ELECTRIC DRIVES-II	37	50	1
14A81D4305	H4303	DIGITAL CONTROLLERS	31	31	1
14A81D4305	H4304	CUSTOM POWER DEVICES	38	32	1
14A81D4305	H4305	RENEWABLE ENERGY SYSTEMS ELECTIVE-III	37	35	1
14A81D4305	H4310	POWER CONVERTERS & DRIVES LAB	39	59	1
14A81D4305	H5605	SMART GRID	39	33	1
14A81D4306	H4301	SWITCHED MODE POWER CONVERSION	40	6	0
14A81D4306	H4302	ELECTRIC DRIVES-II	39	49	1
14A81D4306	H4303	DIGITAL CONTROLLERS	35	37	1
14A81D4306	H4304	CUSTOM POWER DEVICES	37	39	1
14A81D4306	H4305	RENEWABLE ENERGY SYSTEMS ELECTIVE-III	25	32	1
14A81D4306	H4310	POWER CONVERTERS & DRIVES LAB	38	58	1
14A81D4306	H5605	SMART GRID	36	29	1
14A81D4307	H4301	SWITCHED MODE POWER CONVERSION	40	32	1
14A81D4307	H4302	ELECTRIC DRIVES-II	38	53	1
14A81D4307	H4303	DIGITAL CONTROLLERS	37	39	1
14A81D4307	H4304	CUSTOM POWER DEVICES	38	42	1
14A81D4307	H4305	RENEWABLE ENERGY SYSTEMS ELECTIVE-III	39	42	1
14A81D4307	H4310	POWER CONVERTERS & DRIVES LAB	40	60	1
14A81D4307	H5605	SMART GRID	40	42	1
14A81D5301	H5601	POWER SYSTEM DYNAMICS AND STABILITY	35	33	1
14A81D5301	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	30	34	1
14A81D5301	H5603	REAL TIME CONTROL OF POWER SYSTEMS	34	29	1
14A81D5301	H5604	ADVANCED POWER SYSTEM PROTECTION	39	26	1
14A81D5301	H5605	SMART GRID	31	13	0
14A81D5301	H5610	HIGH VOLTAGE TESTING TECHNIQUES	36	24	1
14A81D5301	H5613	POWER SYSTEMS LAB	39	60	1
14A81D5302	H5601	POWER SYSTEM DYNAMICS AND STABILITY	36	50	1
14A81D5302	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	38	40	1
14A81D5302	H5603	REAL TIME CONTROL OF POWER SYSTEMS	36	40	1
14A81D5302	H5604	ADVANCED POWER SYSTEM PROTECTION	40	32	1
14A81D5302	H5605	SMART GRID	36	27	1
14A81D5302	H5610	HIGH VOLTAGE TESTING TECHNIQUES	39	28	1
14A81D5302	H5613	POWER SYSTEMS LAB	40	60	1
14A81D5303	H5601	POWER SYSTEM DYNAMICS AND STABILITY	35	36	1
14A81D5303	H5602	FLEXIBLE AC TRANSMISSION SYSTEMS	35	39	1
14A81D5303	H5603	REAL TIME CONTROL OF POWER SYSTEMS	33	37	1

Htno	Subcode	Subname	Internal	External	credits
14A81D5303	H5604	ADVANCED POWER SYSTEM PROTECTION	39	26	1
14A81D5303	H5605	SMART GRID	38	35	1
14A81D5303	H5610	HIGH VOLTAGE TESTING TECHNIQUES	37	27	1
14A81D5303	H5613	POWER SYSTEMS LAB	39	60	1
14A81D5801	H0501	DATA WAREHOUSING AND DATA MINING	33	35	1
14A81D5801	H0507	HUMAN COMPUTER INTERACTION	35	34	1
14A81D5801	H2508	CLOUD COMPUTING	28	28	1
14A81D5801	H4002	INFORMATION SECURITY	27	27	1
14A81D5801	H5801	COMPUTER NETWORKS	36	33	1
14A81D5801	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	28	24	1
14A81D5801	H5806	CSE LAB-2	34	48	1
14A81D5802	H0501	DATA WAREHOUSING AND DATA MINING	34	34	1
14A81D5802	H0507	HUMAN COMPUTER INTERACTION	33	32	1
14A81D5802	H2508	CLOUD COMPUTING	31	38	1
14A81D5802	H4002	INFORMATION SECURITY	31	31	1
14A81D5802	H5801	COMPUTER NETWORKS	34	34	1
14A81D5802	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	29	29	1
14A81D5802	H5806	CSE LAB-2	35	52	1
14A81D5803	H0501	DATA WAREHOUSING AND DATA MINING	35	44	1
14A81D5803	H0507	HUMAN COMPUTER INTERACTION	38	37	1
14A81D5803	H2508	CLOUD COMPUTING	37	46	1
14A81D5803	H4002	INFORMATION SECURITY	38	39	1
14A81D5803	H5801	COMPUTER NETWORKS	38	35	1
14A81D5803	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	39	36	1
14A81D5803	H5806	CSE LAB-2	36	59	1
14A81D5804	H0501	DATA WAREHOUSING AND DATA MINING	34	34	1
14A81D5804	H0507	HUMAN COMPUTER INTERACTION	35	39	1
14A81D5804	H2508	CLOUD COMPUTING	32	41	1
14A81D5804	H4002	INFORMATION SECURITY	33	35	1
14A81D5804	H5801	COMPUTER NETWORKS	35	33	1
14A81D5804	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	28	27	1
14A81D5804	H5806	CSE LAB-2	32	51	1
14A81D5805	H0501	DATA WAREHOUSING AND DATA MINING	34	44	1
14A81D5805	H0507	HUMAN COMPUTER INTERACTION	38	39	1
14A81D5805	H2508	CLOUD COMPUTING	37	48	1
14A81D5805	H4002	INFORMATION SECURITY	37	41	1
14A81D5805	H5801	COMPUTER NETWORKS	37	39	1
14A81D5805	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	38	43	1
14A81D5805	H5806	CSE LAB-2	34	53	1
14A81D5806	H0501	DATA WAREHOUSING AND DATA MINING	35	44	1
14A81D5806	H0507	HUMAN COMPUTER INTERACTION	37	39	1
14A81D5806	H2508	CLOUD COMPUTING	32	34	1
14A81D5806	H4002	INFORMATION SECURITY	31	39	1
14A81D5806	H5801	COMPUTER NETWORKS	38	37	1
14A81D5806	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	28	35	1
14A81D5806	H5806	CSE LAB-2	32	52	1
14A81D5807	H0501	DATA WAREHOUSING AND DATA MINING	33	38	1
14A81D5807	H0507	HUMAN COMPUTER INTERACTION	36	36	1
14A81D5807	H2508	CLOUD COMPUTING	34	41	1
14A81D5807	H4002	INFORMATION SECURITY	29	39	1
14A81D5807	H5801	COMPUTER NETWORKS	35	33	1

Htno	Subcode	Subname	Internal	External	credits
14A81D5807	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	28	31	1
14A81D5807	H5806	CSE LAB-2	35	51	1
14A81D5808	H0501	DATA WAREHOUSING AND DATA MINING	35	38	1
14A81D5808	H0507	HUMAN COMPUTER INTERACTION	36	33	1
14A81D5808	H2508	CLOUD COMPUTING	34	41	1
14A81D5808	H4002	INFORMATION SECURITY	33	38	1
14A81D5808	H5801	COMPUTER NETWORKS	36	35	1
14A81D5808	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	33	32	1
14A81D5808	H5806	CSE LAB-2	35	59	1
14A81D5809	H0501	DATA WAREHOUSING AND DATA MINING	33	31	1
14A81D5809	H0507	HUMAN COMPUTER INTERACTION	37	37	1
14A81D5809	H2508	CLOUD COMPUTING	33	43	1
14A81D5809	H4002	INFORMATION SECURITY	29	37	1
14A81D5809	H5801	COMPUTER NETWORKS	36	31	1
14A81D5809	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	31	29	1
14A81D5809	H5806	CSE LAB-2	35	51	1
14A81D5810	H0501	DATA WAREHOUSING AND DATA MINING	34	44	1
14A81D5810	H0507	HUMAN COMPUTER INTERACTION	39	39	1
14A81D5810	H2508	CLOUD COMPUTING	36	45	1
14A81D5810	H4002	INFORMATION SECURITY	37	50	1
14A81D5810	H5801	COMPUTER NETWORKS	38	37	1
14A81D5810	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	37	35	1
14A81D5810	H5806	CSE LAB-2	38	59	1
14A81D5811	H0501	DATA WAREHOUSING AND DATA MINING	33	41	1
14A81D5811	H0507	HUMAN COMPUTER INTERACTION	36	38	1
14A81D5811	H2508	CLOUD COMPUTING	34	40	1
14A81D5811	H4002	INFORMATION SECURITY	34	38	1
14A81D5811	H5801	COMPUTER NETWORKS	34	37	1
14A81D5811	H5804	OBJECT ORIENTED ANALYSIS AND DESIGN ELEC	32	36	1
14A81D5811	H5806	CSE LAB-2	35	59	1
14A81D6801	H6801	EMBEDDED-C	30	41	1
14A81D6801	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	33	40	1
14A81D6801	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	33	38	1
14A81D6801	H6804	DESIGN FOR TESTABILITY	31	29	1
14A81D6801	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	31	36	1
14A81D6801	H6809	LOW POWER VLSI DESIGN	38	30	1
14A81D6801	H6811	EMBEDDED SYSTEMS LAB	36	56	1
14A81D6802	H6801	EMBEDDED-C	29	40	1
14A81D6802	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	26	37	1
14A81D6802	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	30	43	1
14A81D6802	H6804	DESIGN FOR TESTABILITY	26	24	1
14A81D6802	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	27	24	1
14A81D6802	H6809	LOW POWER VLSI DESIGN	38	25	1
14A81D6802	H6811	EMBEDDED SYSTEMS LAB	35	54	1
14A81D6803	H6801	EMBEDDED-C	30	46	1
14A81D6803	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	26	31	1
14A81D6803	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	35	30	1
14A81D6803	H6804	DESIGN FOR TESTABILITY	28	26	1
14A81D6803	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	32	34	1
14A81D6803	H6809	LOW POWER VLSI DESIGN	31	25	1
14A81D6803	H6811	EMBEDDED SYSTEMS LAB	36	55	1

Htno	Subcode	Subname	Internal	External	credits
14A81D6804	H6801	EMBEDDED-C	37	50	1
14A81D6804	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	40	43	1
14A81D6804	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	34	45	1
14A81D6804	H6804	DESIGN FOR TESTABILITY	35	47	1
14A81D6804	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	33	39	1
14A81D6804	H6809	LOW POWER VLSI DESIGN	39	37	1
14A81D6804	H6811	EMBEDDED SYSTEMS LAB	38	54	1
14A81D6805	H6801	EMBEDDED-C	26	37	1
14A81D6805	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	22	28	1
14A81D6805	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	28	39	1
14A81D6805	H6804	DESIGN FOR TESTABILITY	30	10	0
14A81D6805	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	24	26	1
14A81D6805	H6809	LOW POWER VLSI DESIGN	35	12	0
14A81D6805	H6811	EMBEDDED SYSTEMS LAB	33	56	1
14A81D6806	H6801	EMBEDDED-C	30	38	1
14A81D6806	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	30	39	1
14A81D6806	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	26	36	1
14A81D6806	H6804	DESIGN FOR TESTABILITY	26	24	1
14A81D6806	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	27	26	1
14A81D6806	H6809	LOW POWER VLSI DESIGN	34	26	1
14A81D6806	H6811	EMBEDDED SYSTEMS LAB	36	57	1
14A81D6807	H6801	EMBEDDED-C	26	38	1
14A81D6807	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	33	42	1
14A81D6807	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	29	35	1
14A81D6807	H6804	DESIGN FOR TESTABILITY	27	24	1
14A81D6807	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	33	24	1
14A81D6807	H6809	LOW POWER VLSI DESIGN	37	26	1
14A81D6807	H6811	EMBEDDED SYSTEMS LAB	33	58	1
14A81D6808	H6801	EMBEDDED-C	30	48	1
14A81D6808	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	39	35	1
14A81D6808	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	37	40	1
14A81D6808	H6804	DESIGN FOR TESTABILITY	34	24	1
14A81D6808	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	28	29	1
14A81D6808	H6809	LOW POWER VLSI DESIGN	38	27	1
14A81D6808	H6811	EMBEDDED SYSTEMS LAB	36	56	1
14A81D6809	H6801	EMBEDDED-C	26	40	1
14A81D6809	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	29	24	1
14A81D6809	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	23	27	1
14A81D6809	H6804	DESIGN FOR TESTABILITY	27	8	0
14A81D6809	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	27	16	0
14A81D6809	H6809	LOW POWER VLSI DESIGN	37	24	1
14A81D6809	H6811	EMBEDDED SYSTEMS LAB	33	55	1
14A81D6810	H6801	EMBEDDED-C	27	44	1
14A81D6810	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	33	33	1
14A81D6810	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	33	44	1
14A81D6810	H6804	DESIGN FOR TESTABILITY	32	25	1
14A81D6810	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	27	31	1
14A81D6810	H6809	LOW POWER VLSI DESIGN	39	25	1
14A81D6810	H6811	EMBEDDED SYSTEMS LAB	34	56	1
14A81D6811	H6801	EMBEDDED-C	28	41	1
14A81D6811	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	29	33	1

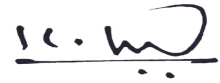
Htno	Subcode	Subname	Internal	External	credits
14A81D6811	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	27	34	1
14A81D6811	H6804	DESIGN FOR TESTABILITY	27	15	0
14A81D6811	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	27	37	1
14A81D6811	H6809	LOW POWER VLSI DESIGN	36	26	1
14A81D6811	H6811	EMBEDDED SYSTEMS LAB	35	54	1
14A81D6812	H6801	EMBEDDED-C	29	35	1
14A81D6812	H6802	CMOS MIXED SIGNAL CIRCUIT DESIGN	25	25	1
14A81D6812	H6803	EMBEDDED REAL TIME OPERATING SYSTEMS	30	35	1
14A81D6812	H6804	DESIGN FOR TESTABILITY	29	24	1
14A81D6812	H6805	DIGITAL SIGNAL PROCESSORS & ARCHITECTURE	29	28	1
14A81D6812	H6809	LOW POWER VLSI DESIGN	37	25	1
14A81D6812	H6811	EMBEDDED SYSTEMS LAB	35	58	1

**\*\*Note:1)For Recounting/Revaluation/Challenge By Revaluation Apply through College PORTAL by Examination Incharge only )**

**\*\*NOTE:2 [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: 23-02-2016]**

**\*\*NOTE:3 [Please inform to the students enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation ]**

Date:15-02-2016



Controller of Examinations