



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for M.TECH I SEMESTER (R13/R09) REGULAR/SUPPLEMENTARY EXAMINATIONS FEB-2016

College name: VASAVI,TADEPALLIGUDEM:A8

Discrepancy pertaining to this results supposed to be cleared on or before 06-08-2016 with following documents at CE Office,JNTUK,Kakinada

- Online Registration Proof
- Hallticket
- DForm(Online)
- DForm(Offline)
- Attendance Sheet
- Any Other supporting Documents

Htno	Subcode	Subname	Internal	External	STATUS
12A81D3804	C3805	ADVANCED DIGITAL SIGNAL PROCESSING	28	25	PASS
13A81D3801	G4505	STATISTICAL SIGNAL PROCESSING	26	0	FAIL
13A81D3801	G8205	DETECTION & ESTIMATION THEORY	28	5	FAIL
14A81D3804	G4504	DIGITAL DATA COMMUNICATIONS	32	30	PASS
14A81D3804	G4505	STATISTICAL SIGNAL PROCESSING	28	-1	FAIL
14A81D6805	G6803	CMOS ANALOG IC DESIGN	26	29	PASS
14A81D6805	G6809	CMOS DIGITAL IC DESIGN	26	37	PASS
14A81D6806	G6803	CMOS ANALOG IC DESIGN	26	31	PASS
15A81D1501	G1501	COMPUTATIONAL METHODS IN ENGINEERING	38	9	FAIL
15A81D1501	G1502	ADVANCED MECHANICS OF SOLIDS	36	7	FAIL
15A81D1501	G1503	ADVANCED MECHANISMS	37	31	PASS
15A81D1501	G1504	MECHANICAL VIBRATIONS	38	10	FAIL
15A81D1501	G1505	DESIGN WITH ADVANCED MATERIALS	36	7	FAIL
15A81D1501	G1507	PRODUCT DESIGN	33	15	FAIL
15A81D1501	G1510	MACHINE DYNAMICS LAB	36	54	PASS
15A81D1502	G1501	COMPUTATIONAL METHODS IN ENGINEERING	36	-1	FAIL
15A81D1502	G1502	ADVANCED MECHANICS OF SOLIDS	37	24	PASS
15A81D1502	G1503	ADVANCED MECHANISMS	37	29	PASS
15A81D1502	G1504	MECHANICAL VIBRATIONS	38	-1	FAIL
15A81D1502	G1505	DESIGN WITH ADVANCED MATERIALS	35	25	PASS
15A81D1502	G1507	PRODUCT DESIGN	31	-1	FAIL
15A81D1502	G1510	MACHINE DYNAMICS LAB	37	51	PASS
15A81D1503	G1501	COMPUTATIONAL METHODS IN ENGINEERING	37	-1	FAIL
15A81D1503	G1502	ADVANCED MECHANICS OF SOLIDS	35	9	FAIL
15A81D1503	G1503	ADVANCED MECHANISMS	37	31	PASS
15A81D1503	G1504	MECHANICAL VIBRATIONS	35	-1	FAIL
15A81D1503	G1505	DESIGN WITH ADVANCED MATERIALS	34	34	PASS
15A81D1503	G1507	PRODUCT DESIGN	32	-1	FAIL
15A81D1503	G1510	MACHINE DYNAMICS LAB	37	54	PASS
15A81D1504	G1501	COMPUTATIONAL METHODS IN ENGINEERING	37	28	PASS
15A81D1504	G1502	ADVANCED MECHANICS OF SOLIDS	35	28	PASS
15A81D1504	G1503	ADVANCED MECHANISMS	37	43	PASS
15A81D1504	G1504	MECHANICAL VIBRATIONS	40	33	PASS
15A81D1504	G1505	DESIGN WITH ADVANCED MATERIALS	35	36	PASS

Htno	Subcode	Subname	Internal	External	STATUS
15A81D1504	G1507	PRODUCT DESIGN	32	31	PASS
15A81D1504	G1510	MACHINE DYNAMICS LAB	35	55	PASS
15A81D1505	G1501	COMPUTATIONAL METHODS IN ENGINEERING	37	16	FAIL
15A81D1505	G1502	ADVANCED MECHANICS OF SOLIDS	40	31	PASS
15A81D1505	G1503	ADVANCED MECHANISMS	38	34	PASS
15A81D1505	G1504	MECHANICAL VIBRATIONS	39	36	PASS
15A81D1505	G1505	DESIGN WITH ADVANCED MATERIALS	35	30	PASS
15A81D1505	G1507	PRODUCT DESIGN	35	32	PASS
15A81D1505	G1510	MACHINE DYNAMICS LAB	38	55	PASS
15A81D1506	G1501	COMPUTATIONAL METHODS IN ENGINEERING	38	14	FAIL
15A81D1506	G1502	ADVANCED MECHANICS OF SOLIDS	37	24	PASS
15A81D1506	G1503	ADVANCED MECHANISMS	37	33	PASS
15A81D1506	G1504	MECHANICAL VIBRATIONS	37	24	PASS
15A81D1506	G1505	DESIGN WITH ADVANCED MATERIALS	35	25	PASS
15A81D1506	G1507	PRODUCT DESIGN	30	26	PASS
15A81D1506	G1510	MACHINE DYNAMICS LAB	38	53	PASS
15A81D3801	G0603	DESIGN & SIMULATION LAB	28	55	PASS
15A81D3801	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	28	8	FAIL
15A81D3801	G4504	DIGITAL DATA COMMUNICATIONS	28	30	PASS
15A81D3801	G4505	STATISTICAL SIGNAL PROCESSING	29	29	PASS
15A81D3801	G6802	VLSI TECHNOLOGY AND DESIGN	26	46	PASS
15A81D3801	G6806	DIGITAL SYSTEM DESIGN	30	30	PASS
15A81D3801	G8205	DETECTION & ESTIMATION THEORY	34	35	PASS
15A81D3802	G0603	DESIGN & SIMULATION LAB	36	56	PASS
15A81D3802	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	33	30	PASS
15A81D3802	G4504	DIGITAL DATA COMMUNICATIONS	36	38	PASS
15A81D3802	G4505	STATISTICAL SIGNAL PROCESSING	37	49	PASS
15A81D3802	G6802	VLSI TECHNOLOGY AND DESIGN	29	52	PASS
15A81D3802	G6806	DIGITAL SYSTEM DESIGN	38	36	PASS
15A81D3802	G8205	DETECTION & ESTIMATION THEORY	40	36	PASS
15A81D3803	G0603	DESIGN & SIMULATION LAB	32	50	PASS
15A81D3803	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	29	24	PASS
15A81D3803	G4504	DIGITAL DATA COMMUNICATIONS	28	30	PASS
15A81D3803	G4505	STATISTICAL SIGNAL PROCESSING	29	39	PASS
15A81D3803	G6802	VLSI TECHNOLOGY AND DESIGN	24	41	PASS
15A81D3803	G6806	DIGITAL SYSTEM DESIGN	29	42	PASS
15A81D3803	G8205	DETECTION & ESTIMATION THEORY	27	33	PASS
15A81D3804	G0603	DESIGN & SIMULATION LAB	28	49	PASS
15A81D3804	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	26	13	FAIL
15A81D3804	G4504	DIGITAL DATA COMMUNICATIONS	27	33	PASS
15A81D3804	G4505	STATISTICAL SIGNAL PROCESSING	26	28	PASS
15A81D3804	G6802	VLSI TECHNOLOGY AND DESIGN	15	24	FAIL
15A81D3804	G6806	DIGITAL SYSTEM DESIGN	32	30	PASS
15A81D3804	G8205	DETECTION & ESTIMATION THEORY	26	24	PASS
15A81D3805	G0603	DESIGN & SIMULATION LAB	39	56	PASS
15A81D3805	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	31	24	PASS
15A81D3805	G4504	DIGITAL DATA COMMUNICATIONS	35	37	PASS
15A81D3805	G4505	STATISTICAL SIGNAL PROCESSING	29	48	PASS
15A81D3805	G6802	VLSI TECHNOLOGY AND DESIGN	29	36	PASS
15A81D3805	G6806	DIGITAL SYSTEM DESIGN	39	30	PASS
15A81D3805	G8205	DETECTION & ESTIMATION THEORY	33	34	PASS

Htno	Subcode	Subname	Internal	External	STATUS
15A81D4301	G4301	ELECTRICAL MACHINE MODELING & ANALYSIS	38	27	PASS
15A81D4301	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	34	24	PASS
15A81D4301	G4303	ELECTRIC DRIVES-I	33	32	PASS
15A81D4301	G4304	FLEXIBLE AC TRANSMISSION SYSTEMS	33	32	PASS
15A81D4301	G4309	SYSTEMS SIMULATION LAB	37	45	PASS
15A81D4301	G5602	HVDC TRANSMISSION	31	24	PASS
15A81D4301	G5614	MODERN CONTROL THEORY	33	30	PASS
15A81D4302	G4301	ELECTRICAL MACHINE MODELING & ANALYSIS	39	32	PASS
15A81D4302	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	39	33	PASS
15A81D4302	G4303	ELECTRIC DRIVES-I	39	39	PASS
15A81D4302	G4304	FLEXIBLE AC TRANSMISSION SYSTEMS	37	43	PASS
15A81D4302	G4309	SYSTEMS SIMULATION LAB	38	54	PASS
15A81D4302	G5602	HVDC TRANSMISSION	34	43	PASS
15A81D4302	G5614	MODERN CONTROL THEORY	38	40	PASS
15A81D4303	G4301	ELECTRICAL MACHINE MODELING & ANALYSIS	39	49	PASS
15A81D4303	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	39	41	PASS
15A81D4303	G4303	ELECTRIC DRIVES-I	39	51	PASS
15A81D4303	G4304	FLEXIBLE AC TRANSMISSION SYSTEMS	39	44	PASS
15A81D4303	G4309	SYSTEMS SIMULATION LAB	40	59	PASS
15A81D4303	G5602	HVDC TRANSMISSION	38	53	PASS
15A81D4303	G5614	MODERN CONTROL THEORY	39	36	PASS
15A81D4304	G4301	ELECTRICAL MACHINE MODELING & ANALYSIS	38	2	FAIL
15A81D4304	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	36	7	FAIL
15A81D4304	G4303	ELECTRIC DRIVES-I	34	24	PASS
15A81D4304	G4304	FLEXIBLE AC TRANSMISSION SYSTEMS	35	14	FAIL
15A81D4304	G4309	SYSTEMS SIMULATION LAB	37	50	PASS
15A81D4304	G5602	HVDC TRANSMISSION	28	35	PASS
15A81D4304	G5614	MODERN CONTROL THEORY	33	4	FAIL
15A81D5301	G5601	MICROPROCESSORS & MICROCONTROLLERS	14	36	PASS
15A81D5301	G5602	HVDC TRANSMISSION	22	24	FAIL
15A81D5301	G5603	POWER SYSTEM OPERATION AND CONTROL	38	32	PASS
15A81D5301	G5604	REACTIVE POWER COMPENSATION & MANAGEMENT	30	28	PASS
15A81D5301	G5605	ELECTRICAL DISTRIBUTION SYSTEMS	36	31	PASS
15A81D5301	G5614	MODERN CONTROL THEORY	36	36	PASS
15A81D5301	G5615	SIMULATION LAB	39	59	PASS
15A81D5302	G5601	MICROPROCESSORS & MICROCONTROLLERS	31	36	PASS
15A81D5302	G5602	HVDC TRANSMISSION	28	43	PASS
15A81D5302	G5603	POWER SYSTEM OPERATION AND CONTROL	35	32	PASS
15A81D5302	G5604	REACTIVE POWER COMPENSATION & MANAGEMENT	37	28	PASS
15A81D5302	G5605	ELECTRICAL DISTRIBUTION SYSTEMS	33	28	PASS
15A81D5302	G5614	MODERN CONTROL THEORY	32	34	PASS
15A81D5302	G5615	SIMULATION LAB	39	59	PASS
15A81D5303	G5601	MICROPROCESSORS & MICROCONTROLLERS	30	46	PASS
15A81D5303	G5602	HVDC TRANSMISSION	33	48	PASS
15A81D5303	G5603	POWER SYSTEM OPERATION AND CONTROL	38	36	PASS
15A81D5303	G5604	REACTIVE POWER COMPENSATION & MANAGEMENT	38	36	PASS
15A81D5303	G5605	ELECTRICAL DISTRIBUTION SYSTEMS	36	16	FAIL
15A81D5303	G5614	MODERN CONTROL THEORY	35	34	PASS
15A81D5303	G5615	SIMULATION LAB	39	59	PASS
15A81D5304	G5601	MICROPROCESSORS & MICROCONTROLLERS	18	32	PASS
15A81D5304	G5602	HVDC TRANSMISSION	22	26	FAIL

Htno	Subcode	Subname	Internal	External	STATUS
15A81D5304	G5603	POWER SYSTEM OPERATION AND CONTROL	34	18	FAIL
15A81D5304	G5604	REACTIVE POWER COMPENSATION & MANAGEMENT	21	17	FAIL
15A81D5304	G5605	ELECTRICAL DISTRIBUTION SYSTEMS	27	10	FAIL
15A81D5304	G5614	MODERN CONTROL THEORY	23	14	FAIL
15A81D5304	G5615	SIMULATION LAB	39	58	PASS
15A81D5305	G5601	MICROPROCESSORS & MICROCONTROLLERS	26	40	PASS
15A81D5305	G5602	HVDC TRANSMISSION	35	37	PASS
15A81D5305	G5603	POWER SYSTEM OPERATION AND CONTROL	37	35	PASS
15A81D5305	G5604	REACTIVE POWER COMPENSATION & MANAGEMENT	37	32	PASS
15A81D5305	G5605	ELECTRICAL DISTRIBUTION SYSTEMS	33	36	PASS
15A81D5305	G5614	MODERN CONTROL THEORY	35	29	PASS
15A81D5305	G5615	SIMULATION LAB	39	58	PASS
15A81D5306	G5601	MICROPROCESSORS & MICROCONTROLLERS	26	40	PASS
15A81D5306	G5602	HVDC TRANSMISSION	30	32	PASS
15A81D5306	G5603	POWER SYSTEM OPERATION AND CONTROL	37	37	PASS
15A81D5306	G5604	REACTIVE POWER COMPENSATION & MANAGEMENT	37	26	PASS
15A81D5306	G5605	ELECTRICAL DISTRIBUTION SYSTEMS	33	30	PASS
15A81D5306	G5614	MODERN CONTROL THEORY	32	28	PASS
15A81D5306	G5615	SIMULATION LAB	39	59	PASS
15A81D5801	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	33	41	PASS
15A81D5801	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	38	46	PASS
15A81D5801	G0503	DATA BASE MANAGEMENT SYSTEMS	39	52	PASS
15A81D5801	G0504	OPERATING SYSTEM	36	47	PASS
15A81D5801	G0505	SOFTWARE ENGINEERING	36	42	PASS
15A81D5801	G4001	ADVANCED DATA STRUCTURES	31	38	PASS
15A81D5801	G5801	CSE LAB	34	55	PASS
15A81D5802	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	24	8	FAIL
15A81D5802	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	33	31	PASS
15A81D5802	G0503	DATA BASE MANAGEMENT SYSTEMS	28	33	PASS
15A81D5802	G0504	OPERATING SYSTEM	23	28	PASS
15A81D5802	G0505	SOFTWARE ENGINEERING	26	26	PASS
15A81D5802	G4001	ADVANCED DATA STRUCTURES	15	18	FAIL
15A81D5802	G5801	CSE LAB	24	47	PASS
15A81D5803	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	39	44	PASS
15A81D5803	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	39	49	PASS
15A81D5803	G0503	DATA BASE MANAGEMENT SYSTEMS	40	48	PASS
15A81D5803	G0504	OPERATING SYSTEM	39	53	PASS
15A81D5803	G0505	SOFTWARE ENGINEERING	38	44	PASS
15A81D5803	G4001	ADVANCED DATA STRUCTURES	39	48	PASS
15A81D5803	G5801	CSE LAB	38	58	PASS
15A81D5804	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	36	27	PASS
15A81D5804	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	35	40	PASS
15A81D5804	G0503	DATA BASE MANAGEMENT SYSTEMS	33	37	PASS
15A81D5804	G0504	OPERATING SYSTEM	32	29	PASS
15A81D5804	G0505	SOFTWARE ENGINEERING	35	36	PASS
15A81D5804	G4001	ADVANCED DATA STRUCTURES	24	27	PASS
15A81D5804	G5801	CSE LAB	35	52	PASS
15A81D5805	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	22	14	FAIL
15A81D5805	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	27	11	FAIL
15A81D5805	G0503	DATA BASE MANAGEMENT SYSTEMS	24	11	FAIL
15A81D5805	G0504	OPERATING SYSTEM	24	13	FAIL

Htno	Subcode	Subname	Internal	External	STATUS
15A81D5805	G0505	SOFTWARE ENGINEERING	30	28	PASS
15A81D5805	G4001	ADVANCED DATA STRUCTURES	18	24	FAIL
15A81D5805	G5801	CSE LAB	30	45	PASS
15A81D5806	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	33	25	PASS
15A81D5806	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	36	40	PASS
15A81D5806	G0503	DATA BASE MANAGEMENT SYSTEMS	36	38	PASS
15A81D5806	G0504	OPERATING SYSTEM	34	37	PASS
15A81D5806	G0505	SOFTWARE ENGINEERING	37	26	PASS
15A81D5806	G4001	ADVANCED DATA STRUCTURES	32	24	PASS
15A81D5806	G5801	CSE LAB	32	54	PASS
15A81D5807	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	31	24	PASS
15A81D5807	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	38	41	PASS
15A81D5807	G0503	DATA BASE MANAGEMENT SYSTEMS	36	33	PASS
15A81D5807	G0504	OPERATING SYSTEM	18	41	PASS
15A81D5807	G0505	SOFTWARE ENGINEERING	34	37	PASS
15A81D5807	G4001	ADVANCED DATA STRUCTURES	28	25	PASS
15A81D5807	G5801	CSE LAB	36	56	PASS
15A81D5808	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	31	24	PASS
15A81D5808	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	30	40	PASS
15A81D5808	G0503	DATA BASE MANAGEMENT SYSTEMS	34	33	PASS
15A81D5808	G0504	OPERATING SYSTEM	30	47	PASS
15A81D5808	G0505	SOFTWARE ENGINEERING	32	42	PASS
15A81D5808	G4001	ADVANCED DATA STRUCTURES	34	26	PASS
15A81D5808	G5801	CSE LAB	39	58	PASS
15A81D5809	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	35	31	PASS
15A81D5809	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	37	40	PASS
15A81D5809	G0503	DATA BASE MANAGEMENT SYSTEMS	36	35	PASS
15A81D5809	G0504	OPERATING SYSTEM	36	37	PASS
15A81D5809	G0505	SOFTWARE ENGINEERING	35	42	PASS
15A81D5809	G4001	ADVANCED DATA STRUCTURES	29	30	PASS
15A81D5809	G5801	CSE LAB	38	54	PASS
15A81D5810	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	36	24	PASS
15A81D5810	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	36	48	PASS
15A81D5810	G0503	DATA BASE MANAGEMENT SYSTEMS	38	51	PASS
15A81D5810	G0504	OPERATING SYSTEM	36	45	PASS
15A81D5810	G0505	SOFTWARE ENGINEERING	37	44	PASS
15A81D5810	G4001	ADVANCED DATA STRUCTURES	32	37	PASS
15A81D5810	G5801	CSE LAB	38	57	PASS
15A81D5811	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	33	30	PASS
15A81D5811	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	36	44	PASS
15A81D5811	G0503	DATA BASE MANAGEMENT SYSTEMS	33	7	FAIL
15A81D5811	G0504	OPERATING SYSTEM	32	40	PASS
15A81D5811	G0505	SOFTWARE ENGINEERING	34	40	PASS
15A81D5811	G4001	ADVANCED DATA STRUCTURES	22	28	PASS
15A81D5811	G5801	CSE LAB	35	48	PASS
15A81D6801	G6801	MICROCONTROLLERS FOR EMBEDDED SYSTEM DES	27	24	PASS
15A81D6801	G6802	VLSI TECHNOLOGY AND DESIGN	24	44	PASS
15A81D6801	G6803	CMOS ANALOG IC DESIGN	29	31	PASS
15A81D6801	G6804	CPLD AND FPGA ARCHITECTURES AND APPLICAT	22	29	PASS
15A81D6801	G6806	DIGITAL SYSTEM DESIGN	38	36	PASS
15A81D6801	G6809	CMOS DIGITAL IC DESIGN	24	39	PASS

Htno	Subcode	Subname	Internal	External	STATUS
15A81D6801	G6811	VLSI LABORATORY LAB	35	49	PASS
15A81D6802	G6801	MICROCONTROLLERS FOR EMBEDDED SYSTEM DES	31	17	FAIL
15A81D6802	G6802	VLSI TECHNOLOGY AND DESIGN	32	40	PASS
15A81D6802	G6803	CMOS ANALOG IC DESIGN	31	29	PASS
15A81D6802	G6804	CPLD AND FPGA ARCHITECTURES AND APPLICAT	31	31	PASS
15A81D6802	G6806	DIGITAL SYSTEM DESIGN	38	29	PASS
15A81D6802	G6809	CMOS DIGITAL IC DESIGN	27	36	PASS
15A81D6802	G6811	VLSI LABORATORY LAB	36	55	PASS
15A81D6803	G6801	MICROCONTROLLERS FOR EMBEDDED SYSTEM DES	30	28	PASS
15A81D6803	G6802	VLSI TECHNOLOGY AND DESIGN	34	42	PASS
15A81D6803	G6803	CMOS ANALOG IC DESIGN	34	35	PASS
15A81D6803	G6804	CPLD AND FPGA ARCHITECTURES AND APPLICAT	29	24	PASS
15A81D6803	G6806	DIGITAL SYSTEM DESIGN	33	29	PASS
15A81D6803	G6809	CMOS DIGITAL IC DESIGN	28	35	PASS
15A81D6803	G6811	VLSI LABORATORY LAB	39	56	PASS
15A81D6804	G6801	MICROCONTROLLERS FOR EMBEDDED SYSTEM DES	25	25	PASS
15A81D6804	G6802	VLSI TECHNOLOGY AND DESIGN	24	31	PASS
15A81D6804	G6803	CMOS ANALOG IC DESIGN	25	30	PASS
15A81D6804	G6804	CPLD AND FPGA ARCHITECTURES AND APPLICAT	24	34	PASS
15A81D6804	G6806	DIGITAL SYSTEM DESIGN	32	41	PASS
15A81D6804	G6809	CMOS DIGITAL IC DESIGN	24	35	PASS
15A81D6804	G6811	VLSI LABORATORY LAB	33	49	PASS
15A81D6805	G6801	MICROCONTROLLERS FOR EMBEDDED SYSTEM DES	21	14	FAIL
15A81D6805	G6802	VLSI TECHNOLOGY AND DESIGN	21	25	FAIL
15A81D6805	G6803	CMOS ANALOG IC DESIGN	24	24	FAIL
15A81D6805	G6804	CPLD AND FPGA ARCHITECTURES AND APPLICAT	26	18	FAIL
15A81D6805	G6806	DIGITAL SYSTEM DESIGN	30	15	FAIL
15A81D6805	G6809	CMOS DIGITAL IC DESIGN	24	8	FAIL
15A81D6805	G6811	VLSI LABORATORY LAB	38	54	PASS
15A81D6806	G6801	MICROCONTROLLERS FOR EMBEDDED SYSTEM DES	32	33	PASS
15A81D6806	G6802	VLSI TECHNOLOGY AND DESIGN	27	42	PASS
15A81D6806	G6803	CMOS ANALOG IC DESIGN	33	36	PASS
15A81D6806	G6804	CPLD AND FPGA ARCHITECTURES AND APPLICAT	34	31	PASS
15A81D6806	G6806	DIGITAL SYSTEM DESIGN	37	43	PASS
15A81D6806	G6809	CMOS DIGITAL IC DESIGN	33	43	PASS
15A81D6806	G6811	VLSI LABORATORY LAB	39	56	PASS
15A81D8701	G2201	APPLIED MATHEMATICS	37	34	PASS
15A81D8701	G8701	THEORY OF ELASTICITY	26	35	PASS
15A81D8701	G8702	MATRIX ANALYSIS OF STRUCTURES	30	33	PASS
15A81D8701	G8703	STRUCTURAL DYNAMICS	32	37	PASS
15A81D8701	G8705	SUB-STRUCTURE DESIGN	33	28	PASS
15A81D8701	G8707	REPAIR AND REHABILITATION OF STRUCTURES	34	19	FAIL
15A81D8701	G8710	ADVANCED STRUCTURAL ENGINEERING LAB	38	51	PASS
15A81D8702	G2201	APPLIED MATHEMATICS	36	27	PASS
15A81D8702	G8701	THEORY OF ELASTICITY	36	49	PASS
15A81D8702	G8702	MATRIX ANALYSIS OF STRUCTURES	35	38	PASS
15A81D8702	G8703	STRUCTURAL DYNAMICS	36	32	PASS
15A81D8702	G8705	SUB-STRUCTURE DESIGN	39	32	PASS
15A81D8702	G8707	REPAIR AND REHABILITATION OF STRUCTURES	38	37	PASS
15A81D8702	G8710	ADVANCED STRUCTURAL ENGINEERING LAB	40	50	PASS
15A81D8703	G2201	APPLIED MATHEMATICS	36	27	PASS

Htno	Subcode	Subname	Internal	External	STATUS
15A81D8703	G8701	THEORY OF ELASTICITY	33	31	PASS
15A81D8703	G8702	MATRIX ANALYSIS OF STRUCTURES	35	34	PASS
15A81D8703	G8703	STRUCTURAL DYNAMICS	35	14	FAIL
15A81D8703	G8705	SUB-STRUCTURE DESIGN	36	29	PASS
15A81D8703	G8707	REPAIR AND REHABILITATION OF STRUCTURES	37	24	PASS
15A81D8703	G8710	ADVANCED STRUCTURAL ENGINEERING LAB	40	53	PASS
15A81D8704	G2201	APPLIED MATHEMATICS	23	2	FAIL
15A81D8704	G8701	THEORY OF ELASTICITY	30	24	PASS
15A81D8704	G8702	MATRIX ANALYSIS OF STRUCTURES	25	30	PASS
15A81D8704	G8703	STRUCTURAL DYNAMICS	28	8	FAIL
15A81D8704	G8705	SUB-STRUCTURE DESIGN	30	17	FAIL
15A81D8704	G8707	REPAIR AND REHABILITATION OF STRUCTURES	30	31	PASS
15A81D8704	G8710	ADVANCED STRUCTURAL ENGINEERING LAB	37	50	PASS
15A81D8705	G2201	APPLIED MATHEMATICS	33	28	PASS
15A81D8705	G8701	THEORY OF ELASTICITY	23	42	PASS
15A81D8705	G8702	MATRIX ANALYSIS OF STRUCTURES	28	37	PASS
15A81D8705	G8703	STRUCTURAL DYNAMICS	30	29	PASS
15A81D8705	G8705	SUB-STRUCTURE DESIGN	24	40	PASS
15A81D8705	G8707	REPAIR AND REHABILITATION OF STRUCTURES	30	28	PASS
15A81D8705	G8710	ADVANCED STRUCTURAL ENGINEERING LAB	36	48	PASS
15A81D8706	G2201	APPLIED MATHEMATICS	36	30	PASS
15A81D8706	G8701	THEORY OF ELASTICITY	38	44	PASS
15A81D8706	G8702	MATRIX ANALYSIS OF STRUCTURES	36	42	PASS
15A81D8706	G8703	STRUCTURAL DYNAMICS	39	40	PASS
15A81D8706	G8705	SUB-STRUCTURE DESIGN	39	53	PASS
15A81D8706	G8707	REPAIR AND REHABILITATION OF STRUCTURES	40	38	PASS
15A81D8706	G8710	ADVANCED STRUCTURAL ENGINEERING LAB	40	54	PASS
15A81D8707	G2201	APPLIED MATHEMATICS	31	10	FAIL
15A81D8707	G8701	THEORY OF ELASTICITY	21	26	FAIL
15A81D8707	G8702	MATRIX ANALYSIS OF STRUCTURES	20	25	FAIL
15A81D8707	G8703	STRUCTURAL DYNAMICS	31	16	FAIL
15A81D8707	G8705	SUB-STRUCTURE DESIGN	37	26	PASS
15A81D8707	G8707	REPAIR AND REHABILITATION OF STRUCTURES	32	28	PASS
15A81D8707	G8710	ADVANCED STRUCTURAL ENGINEERING LAB	37	50	PASS

**Note:1)For Recounting/Revaluation/Challenge By Revaluation Apply through College PORTAL by Examination Incharge only)

**NOTE:2 [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: 11-08-2016]

**NOTE:3 [Please inform to the students enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation]



Date:01-08-2016

Controller of Examinations