



# JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

Results for M.Tech (R13/R16) | Semester Regular/Supplementary Examinations December 2016

College: VASAVI,TADEPALLIGUDEM:A8

Discrepancy pertaining to these results are to be submitted on or before 19-07-2017 with following documents at CE(PG) Office,JNTUK,Kakinada

- Online Registration Proof
- Hallticket
- DForm(Online)
- DForm(Offline)
- Attendance Sheet
- Any Other supporting Documents

Htno	Subcode	Subname	Internal	External	credits
13A81D3801	G4505	STATISTICAL SIGNAL PROCESSING	26	30	1
13A81D3801	G8205	DETECTION & ESTIMATION THEORY	28	10	0
14A81D3804	G4505	STATISTICAL SIGNAL PROCESSING	28	41	1
15A81D1501	G1501	COMPUTATIONAL METHODS IN ENGINEERING	38	27	1
15A81D1501	G1502	ADVANCED MECHANICS OF SOLIDS	36	24	1
15A81D1501	G1504	MECHANICAL VIBRATIONS	38	28	1
15A81D1501	G1507	PRODUCT DESIGN	33	24	1
15A81D1502	G1501	COMPUTATIONAL METHODS IN ENGINEERING	36	29	1
15A81D1502	G1504	MECHANICAL VIBRATIONS	38	34	1
15A81D1502	G1507	PRODUCT DESIGN	31	29	1
15A81D1503	G1501	COMPUTATIONAL METHODS IN ENGINEERING	37	27	1
15A81D1503	G1502	ADVANCED MECHANICS OF SOLIDS	35	29	1
15A81D1503	G1504	MECHANICAL VIBRATIONS	35	32	1
15A81D1503	G1507	PRODUCT DESIGN	32	28	1
15A81D3801	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	28	42	1
15A81D3804	G4503	ADVANCED DIGITAL SIGNAL PROCESSING	26	39	1
15A81D3804	G6802	VLSI TECHNOLOGY AND DESIGN	15	23	0
15A81D4304	G4301	ELECTRICAL MACHINE MODELING & ANALYSIS	38	21	0
15A81D4304	G4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	36	11	0
15A81D4304	G4304	FLEXIBLE AC TRANSMISSION SYSTEMS	35	24	1
15A81D4304	G5614	MODERN CONTROL THEORY	33	6	0
15A81D5301	G5602	HVDC TRANSMISSION	22	27	0
15A81D5304	G5602	HVDC TRANSMISSION	22	10	0
15A81D5304	G5604	REACTIVE POWER COMPENSATION & MANAGEMENT	21	16	0
15A81D5304	G5605	ELECTRICAL DISTRIBUTION SYSTEMS	27	0	0
15A81D5304	G5614	MODERN CONTROL THEORY	23	-1	0
15A81D5802	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	24	-1	0
15A81D5802	G4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	15	10	0
15A81D5805	G0501	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	22	11	0
15A81D5805	G0502	COMPUTER ORGANIZATION AND ARCHITECTURE	27	25	1
15A81D5805	G0503	DATA BASE MANAGEMENT SYSTEMS	24	32	1
15A81D5805	G0504	OPERATING SYSTEM	24	26	1
15A81D5805	G4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	18	17	0
15A81D5811	G0503	DATA BASE MANAGEMENT SYSTEMS	33	26	1

Htno	Subcode	Subname	Internal	External	credits
15A81D8704	G2201	APPLIED MATHEMATICS	23	16	0
15A81D8704	G8703	STRUCTURAL DYNAMICS	28	24	1
15A81D8704	G8705	SUB-STRUCTURE DESIGN	30	35	1
15A81D8707	G2201	APPLIED MATHEMATICS	31	19	0
15A81D8707	G8701	THEORY OF ELASTICITY	21	32	1
15A81D8707	G8702	MATRIX ANALYSIS OF STRUCTURES	20	30	1
16A81D1501	I1501	ADVANCED MECHANICS OF SOLIDS	35	25	1
16A81D1501	I1502	ADVANCED MECHANISMS	34	32	1
16A81D1501	I1503	MECHANICAL VIBRATIONS ELECTIVE 1	37	18	0
16A81D1501	I1505	PRODUCT DESIGN ELECTIVE 1	35	25	1
16A81D1501	I1510	DESIGN FOR MANUFACTURING AND ASSEMBLY EL	39	25	1
16A81D1501	I1512	MACHINE DYNAMICS LAB	32	57	1
16A81D1501	I1801	COMPUTATIONAL METHODS IN ENGINEERING ELE	38	30	1
16A81D1502	I1501	ADVANCED MECHANICS OF SOLIDS	33	24	1
16A81D1502	I1502	ADVANCED MECHANISMS	35	24	1
16A81D1502	I1503	MECHANICAL VIBRATIONS ELECTIVE 1	37	27	1
16A81D1502	I1505	PRODUCT DESIGN ELECTIVE 1	34	35	1
16A81D1502	I1510	DESIGN FOR MANUFACTURING AND ASSEMBLY EL	39	25	1
16A81D1502	I1512	MACHINE DYNAMICS LAB	37	55	1
16A81D1502	I1801	COMPUTATIONAL METHODS IN ENGINEERING ELE	38	24	1
16A81D3801	I3801	SYSTEM DESIGN AND DATA COMMUNICATIONS LA	37	51	1
16A81D3801	I4503	ADVANCED DIGITAL SIGNAL PROCESSING	33	24	1
16A81D3801	I4504	DIGITAL DATA COMMUNICATIONS	31	39	1
16A81D3801	I4505	STATISTICAL SIGNAL PROCESSING ELECTIVE 1	33	42	1
16A81D3801	I6801	DIGITAL SYSTEM DESIGN	30	43	1
16A81D3801	I6802	VLSI TECHNOLOGY AND DESIGN ELECTIVE 1	28	24	1
16A81D3801	I8205	DETECTION AND ESTIMATION THEORY	36	46	1
16A81D4301	I4301	ELECTRICAL MACHINE MODELING & ANALYSIS	38	24	1
16A81D4301	I4302	ANALYSIS OF POWER ELECTRONIC CONVERTERS	33	18	0
16A81D4301	I4303	POWER ELECTRONIC CONTROL OF DC DRIVES	36	26	1
16A81D4301	I4304	FLEXIBLE AC TRANSMISSION SYSTEMS	32	34	1
16A81D4301	I4309	SIMULATION LAB	40	56	1
16A81D4301	I5602	HVDC TRANSMISSION	38	17	0
16A81D4301	I5614	MODERN CONTROL THEORY	36	8	0
16A81D5301	I5601	MICROPROCESSORS & MICROCONTROLLERS	33	33	1
16A81D5301	I5602	HVDC TRANSMISSION	38	32	1
16A81D5301	I5603	POWER SYSTEM OPERATION AND CONTROL	39	39	1
16A81D5301	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	34	41	1
16A81D5301	I5607	ANALYSIS OF POWER ELECTRONICS CONVERTERS	36	24	1
16A81D5301	I5614	MODERN CONTROL THEORY	37	24	1
16A81D5301	I5615	SIMULATION LABORATORY	39	56	1
16A81D5302	I5601	MICROPROCESSORS & MICROCONTROLLERS	30	24	1
16A81D5302	I5602	HVDC TRANSMISSION	39	25	1
16A81D5302	I5603	POWER SYSTEM OPERATION AND CONTROL	33	32	1
16A81D5302	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	29	38	1
16A81D5302	I5607	ANALYSIS OF POWER ELECTRONICS CONVERTERS	31	25	1
16A81D5302	I5614	MODERN CONTROL THEORY	34	4	0
16A81D5302	I5615	SIMULATION LABORATORY	38	45	1
16A81D5303	I5601	MICROPROCESSORS & MICROCONTROLLERS	34	28	1
16A81D5303	I5602	HVDC TRANSMISSION	40	29	1
16A81D5303	I5603	POWER SYSTEM OPERATION AND CONTROL	40	42	1

Htno	Subcode	Subname	Internal	External	credits
16A81D5303	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	32	43	1
16A81D5303	I5607	ANALYSIS OF POWER ELECTRONICS CONVERTERS	36	33	1
16A81D5303	I5614	MODERN CONTROL THEORY	36	29	1
16A81D5303	I5615	SIMULATION LABORATORY	40	59	1
16A81D5304	I5601	MICROPROCESSORS & MICROCONTROLLERS	38	24	1
16A81D5304	I5602	HVDC TRANSMISSION	39	26	1
16A81D5304	I5603	POWER SYSTEM OPERATION AND CONTROL	38	25	1
16A81D5304	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	31	31	1
16A81D5304	I5607	ANALYSIS OF POWER ELECTRONICS CONVERTERS	37	24	1
16A81D5304	I5614	MODERN CONTROL THEORY	37	26	1
16A81D5304	I5615	SIMULATION LABORATORY	39	51	1
16A81D5305	I5601	MICROPROCESSORS & MICROCONTROLLERS	28	12	0
16A81D5305	I5602	HVDC TRANSMISSION	36	26	1
16A81D5305	I5603	POWER SYSTEM OPERATION AND CONTROL	33	16	0
16A81D5305	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	22	28	1
16A81D5305	I5607	ANALYSIS OF POWER ELECTRONICS CONVERTERS	32	24	1
16A81D5305	I5614	MODERN CONTROL THEORY	31	15	0
16A81D5305	I5615	SIMULATION LABORATORY	39	48	1
16A81D5306	I5601	MICROPROCESSORS & MICROCONTROLLERS	33	27	1
16A81D5306	I5602	HVDC TRANSMISSION	38	26	1
16A81D5306	I5603	POWER SYSTEM OPERATION AND CONTROL	37	34	1
16A81D5306	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	27	42	1
16A81D5306	I5607	ANALYSIS OF POWER ELECTRONICS CONVERTERS	32	24	1
16A81D5306	I5614	MODERN CONTROL THEORY	35	24	1
16A81D5306	I5615	SIMULATION LABORATORY	38	44	1
16A81D5307	I5601	MICROPROCESSORS & MICROCONTROLLERS	35	35	1
16A81D5307	I5602	HVDC TRANSMISSION	40	34	1
16A81D5307	I5603	POWER SYSTEM OPERATION AND CONTROL	40	31	1
16A81D5307	I5604	REACTIVE POWER COMPENSATION & MANAGEMENT	33	33	1
16A81D5307	I5607	ANALYSIS OF POWER ELECTRONICS CONVERTERS	36	26	1
16A81D5307	I5614	MODERN CONTROL THEORY	37	30	1
16A81D5307	I5615	SIMULATION LABORATORY	40	59	1
16A81D5801	I0504	ADVANCED OPERATING SYSTEM	25	40	1
16A81D5801	I0505	DATA WAREHOUSING AND DATA MINING	31	34	1
16A81D5801	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	23	38	1
16A81D5801	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	33	30	1
16A81D5801	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	29	34	1
16A81D5801	I5803	DATABASE MANAGEMENT SYSTEMS	27	32	1
16A81D5801	I5805	CSE LAB 1	38	50	1
16A81D5802	I0504	ADVANCED OPERATING SYSTEM	36	36	1
16A81D5802	I0505	DATA WAREHOUSING AND DATA MINING	34	39	1
16A81D5802	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	30	36	1
16A81D5802	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	35	35	1
16A81D5802	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	39	26	1
16A81D5802	I5803	DATABASE MANAGEMENT SYSTEMS	37	28	1
16A81D5802	I5805	CSE LAB 1	39	55	1
16A81D5803	I0504	ADVANCED OPERATING SYSTEM	31	34	1
16A81D5803	I0505	DATA WAREHOUSING AND DATA MINING	33	43	1
16A81D5803	I4001	ADVANCED DATA STRUCTURES AND ALGORITHM A	26	31	1
16A81D5803	I5801	MATHEMATICAL FOUNDATIONS OF COMPUTER SCI	32	13	0
16A81D5803	I5802	COMPUTER ORGANIZATION AND ARCHITECTURE	31	27	1

Htno	Subcode	Subname	Internal	External	credits
16A81D5803	I5803	DATABASE MANAGEMENT SYSTEMS	27	28	1
16A81D5803	I5805	CSE LAB 1	38	50	1
16A81D6801	I6801	DIGITAL SYSTEM DESIGN	31	16	0
16A81D6801	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	26	16	0
16A81D6801	I6803	CMOS ANALOG IC DESIGN	35	24	1
16A81D6801	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	37	29	1
16A81D6801	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	29	7	0
16A81D6801	I6810	SYSTEM ON CHIP DESIGN ELECTIVE 2	34	20	0
16A81D6801	I6812	VLSI LABORATORY	35	42	1
16A81D6802	I6801	DIGITAL SYSTEM DESIGN	37	44	1
16A81D6802	I6802	VLSI TECHNOLOGY AND DESIGNELECTIVE 1	31	36	1
16A81D6802	I6803	CMOS ANALOG IC DESIGN	36	24	1
16A81D6802	I6804	HARDWARE SOFTWARE CO-DESIGN ELECTIVE 2	38	41	1
16A81D6802	I6806	CMOS DIGITAL IC DESIGN ELECTIVE 1	34	46	1
16A81D6802	I6810	SYSTEM ON CHIP DESIGN ELECTIVE 2	35	25	1
16A81D6802	I6812	VLSI LABORATORY	38	59	1
16A81D8701	I2201	ADVANCED MATHEMATICS	31	19	0
16A81D8701	I8701	THEORY OF ELASTICITY	26	24	1
16A81D8701	I8702	MATRIX ANALYSIS OF STRUCTURES	28	24	1
16A81D8701	I8703	STRUCTURAL DYNAMICS	23	20	0
16A81D8701	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	23	32	1
16A81D8701	I8707	REPAIR AND REHABILITATION OF STRUCTURES	29	30	1
16A81D8701	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	35	44	1
16A81D8702	I2201	ADVANCED MATHEMATICS	35	43	1
16A81D8702	I8701	THEORY OF ELASTICITY	33	47	1
16A81D8702	I8702	MATRIX ANALYSIS OF STRUCTURES	39	43	1
16A81D8702	I8703	STRUCTURAL DYNAMICS	31	36	1
16A81D8702	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	30	38	1
16A81D8702	I8707	REPAIR AND REHABILITATION OF STRUCTURES	32	32	1
16A81D8702	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	40	48	1
16A81D8703	I2201	ADVANCED MATHEMATICS	34	28	1
16A81D8703	I8701	THEORY OF ELASTICITY	38	37	1
16A81D8703	I8702	MATRIX ANALYSIS OF STRUCTURES	29	38	1
16A81D8703	I8703	STRUCTURAL DYNAMICS	31	48	1
16A81D8703	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	32	40	1
16A81D8703	I8707	REPAIR AND REHABILITATION OF STRUCTURES	35	44	1
16A81D8703	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	38	58	1
16A81D8704	I2201	ADVANCED MATHEMATICS	33	40	1
16A81D8704	I8701	THEORY OF ELASTICITY	35	38	1
16A81D8704	I8702	MATRIX ANALYSIS OF STRUCTURES	33	44	1
16A81D8704	I8703	STRUCTURAL DYNAMICS	28	37	1
16A81D8704	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	30	36	1
16A81D8704	I8707	REPAIR AND REHABILITATION OF STRUCTURES	34	30	1
16A81D8704	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	40	46	1
16A81D8705	I2201	ADVANCED MATHEMATICS	29	24	1
16A81D8705	I8701	THEORY OF ELASTICITY	32	29	1
16A81D8705	I8702	MATRIX ANALYSIS OF STRUCTURES	26	43	1
16A81D8705	I8703	STRUCTURAL DYNAMICS	26	29	1
16A81D8705	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	27	40	1
16A81D8705	I8707	REPAIR AND REHABILITATION OF STRUCTURES	32	32	1
16A81D8705	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	37	50	1

Htno	Subcode	Subname	Internal	External	credits
16A81D8706	I2201	ADVANCED MATHEMATICS	34	24	1
16A81D8706	I8701	THEORY OF ELASTICITY	33	28	1
16A81D8706	I8702	MATRIX ANALYSIS OF STRUCTURES	36	37	1
16A81D8706	I8703	STRUCTURAL DYNAMICS	30	29	1
16A81D8706	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	28	38	1
16A81D8706	I8707	REPAIR AND REHABILITATION OF STRUCTURES	32	34	1
16A81D8706	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	38	57	1
16A81D8707	I2201	ADVANCED MATHEMATICS	38	42	1
16A81D8707	I8701	THEORY OF ELASTICITY	40	50	1
16A81D8707	I8702	MATRIX ANALYSIS OF STRUCTURES	40	38	1
16A81D8707	I8703	STRUCTURAL DYNAMICS	34	40	1
16A81D8707	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	30	41	1
16A81D8707	I8707	REPAIR AND REHABILITATION OF STRUCTURES	34	52	1
16A81D8707	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	40	59	1
16A81D8708	I2201	ADVANCED MATHEMATICS	38	47	1
16A81D8708	I8701	THEORY OF ELASTICITY	39	54	1
16A81D8708	I8702	MATRIX ANALYSIS OF STRUCTURES	40	28	1
16A81D8708	I8703	STRUCTURAL DYNAMICS	35	33	1
16A81D8708	I8705	SUB-STRUCTURE DESIGN ELECTIVE 1	31	38	1
16A81D8708	I8707	REPAIR AND REHABILITATION OF STRUCTURES	34	32	1
16A81D8708	I8710	ADVANCED STRUCTURAL ENGINEERING LAB	40	56	1

Note:1)For Recounting/Revaluation do the Online registration and send the total amount through online transfer  
2)Take Seperate DD for the Challenge Valuation

\*\*Note:1)For Recounting/Revaluation/Challenge By Revaluation Apply through Online([www.jntukresults.edu.in](http://www.jntukresults.edu.in))

\*\*NOTE:2 [Last Date for Apply Recounting/Revaluation/Challenge By Revaluation: **26-07-2017**]

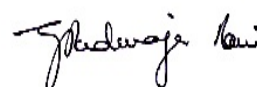
\*\*NOTE:3 [Please inform to the students to enter these subject codes for applying Recounting/Revaluation/Challenge By Revaluation ]

\*\*NOTE:

[-1 in the filed of externals indicates student absent for the respective subject.

-2 in the filed of externals indicates student result is withheld for the respective subject.

-3 in the filed of externals indicates Malpractice for the respective subject. ]



Date:05-07-2017

Controller of Examinations