



**SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)**  
Pedatadepalli, Tadepalligudem – 534 101.  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

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**M. Tech (2022-24 Batch)**

**Students List**

<b>S. No.</b>	<b>Roll Number</b>	<b>Specialization</b>	<b>Name of the Student</b>	<b>Project Title</b>
1.	22A81D4801	ES & VLSI	K. Indira Priya Darshini	Design of Efficient Low Power Test Pattern Generator Circuit for BIST Architecture
2.	22A81D4802	ES & VLSI	R.P.V.S. Aditya	Design and Implementation of Approximate Adders and Subtractors for Low power Application