



SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)

Pedatadepalli, Tadepalligudem – 534 101.

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

M. Tech (2020-22 Batch) Project

Students List

S. No.	Roll Number	Specialization	Name of the Student	Project Title
1.	20A81D6801	VLSI & ES	A. Sailaja	Home Automation using CoAP Protocol.
2.	20A81D6802	VLSI & ES	K. Vijaya Lakshmi	Industrial IOT based Smart Box for Shop Floor Management and Predictive Maintenance.
3.	20A81D6803	VLSI & ES	M. Naga Deepika	Design and Analysis of MEMS Logic Gates using RF–MEMS Switches.
4.	20A81D6805	VLSI & ES	V.V Naga Lakshmi	Design of Memristor based Low Power 3-bit Encoder Using PGL Technology.
5.	20A81D6806	VLSI & ES	V. Jahnvi	High Throughput and Density Optimized Efficient Multi-operand Adder.
6.	20A81D6808	VLSI & ES	K. Lakshmi Meghana	Raspberry PI based System for COVID-19 Indoor Safety Monitoring using OpenCV.
7.	20A81D6809	VLSI & ES	D. Pavani	Low Density and Latency Optimized Scalable Binary Counter / Compressor using Efficient Sorting Network
8.	20A81D6810	VLSI & ES	D. Syamala	An Improved Power Efficient Clock Pulsed D Flip-Flop using Transmission Gate.
9.	20A81D6811	VLSI & ES	D. Ramya	Design of Low Power Approximate Adder and Subtractor for Image Blending Application.
10.	20A81D6812	VLSI & ES	D. Lavanya	Design and Analysis of MEMS based Piezoelectric Energy Harvester for Bio-medical applications.
11.	20A81D6813	VLSI & ES	R. Sai Manasa	A Smart Node to Provide Safety Features for Sewage Workers using Cutting – Edge Technology.
12.	20A81D6814	VLSI & ES	M. Lalitha	Implementation of an IOT Enabled System for Air Quality Monitoring & Prediction Using Deep Learning
13.	20A81D6815	VLSI & ES	P. Ganesh	Design of Efficient Linear Feedback Shift Register for Low Power Applications.
14.	20A81D6816	VLSI & ES	R. D. L. Prasanna	Design of Low Power and High Performance Ternary Content Addressable Memory