



**SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)**  
Pedatadepalli, Tadepalligudem – 534 101.  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

---

**M. Tech (2019-21 Batch) Project Panel-I**

**Students List**

<b>S. No.</b>	<b>Roll Number</b>	<b>Specialization</b>	<b>Name of the Student</b>	<b>Project Title</b>
1.	19A81D6802	VLSI & ES	LAKKOJU BHARANI	Low Power and High-Speed 4 Bit Full Adder Using XOR/XNOR Gates Exploring with the 22nm CMOS Technology
2.	19A81D6803	VLSI & ES	R.N.V.A.S. MANIKANTA	IMPLEMENTATION OF IoT BASED THEFT DETECTION SYSTEM USING MACHINE LEARNING ALGORITHM