

## Organizing Committee

### Patrons

**Sri. G. Satyanarayana**  
*President*

**Sri. Ch.V.V. Subba Rao**  
*Secretary & Correspondent*

### Co-Patron

**Sri. Ch. Apparao**  
*Director – Technical*

### Chairman

**Dr. GVNSR Ratnakara Rao,**  
*Principal*

### Convener

**Dr E Kusuma Kumar,**  
*Professor & HOD, ECE*

### Co-Convener:

**Dr U. Yedukondalu**

*Associate Professor, Department of ECE*

*Convener SVEC HCC & MSME BI*

*Mail id: drykudara@gmail.com*

*8008465666 & 9849437940*

**Organizing Advisory Committee**

**Faculty members of E.C.E.**

## About the College

Sri Vasavi Engineering College (Autonomous)\* was established in the year 2001 by "Sri Vasavi Educational Society" with a desire to share the responsibilities of the society for the development of quality Technical Education. The college is situated at Pedaladepalli, a peaceful and pollution-free location, 5 km away from the main town (Tadepalligudem, West Godavari District).

The town is well known as the commercial hub of Coastal Andhra Pradesh. It is well connected by road (NH16) and rail (Chennai to Kolkata). The college has a sprawling campus of 25.02 acres surrounded by green fields and coconut groves. The college is recognized by AICTE and permanently affiliated to JNTU Kakinada and also recognized by UGC under section 2(f) & 12(B). The College got UGC Autonomous in the year 2018. The college offers UG, PG and Diploma Programs in Engineering (EEE, ECE, CSE, ME & CE, CST and ECT) along with MBA. The college is accredited by NAAC with 'A' Grade. UG Programs offered by the departments of EEE, ME, ECE & CSE are accredited by NBA.

## About the Department

Vision of the Department is to develop the department into a centre of excellence and produce high quality, technically competent and responsible Electronics and Communication engineering. The Departments of ECE is established in the year 2001 with a view to develop quality engineers to meet the current trends.

The Department offers undergraduate program in B.Tech (ECE). The Department also offers PG programme with VLSI & Embedded Systems (VLSI & ES) specialization having an intake of 18. The department has a pool of well qualified, highly experienced and research oriented faculty members covering all the major areas.

## Outcome of the Program

Identify research problems, publish quality research in Signal, Image Processing, Antenna's, VLSI and IoT

## Registration

**Who can attend:** Faculty of various Universities/ Institutes Professionals from Industry, Scientists and Engineers from R&D organizations and PhD students can apply. The participants can register in online.

\*\*\*\*\*No Registration Fees\*\*\*\*\*

## Link for registration:

<https://forms.gle/Vh7M52xhEhvenbz6>

\* Complete your registrations on or before 26<sup>th</sup> May, 2020.

\* Online meeting link will be provided through WhatsApp/Email.

The number of participants will be limited.

Note: Certificates will be issued to those participants who have attended the program.

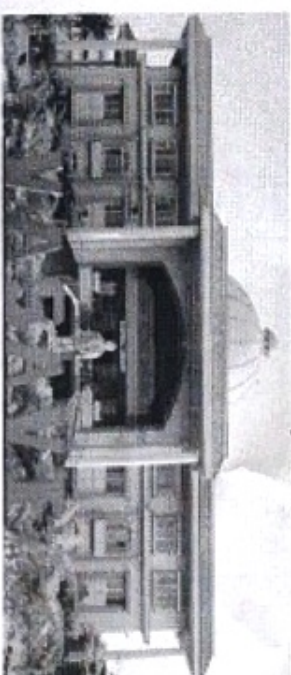
A Three Day National Level Online  
Faculty Development Program

On

“Research Trends in Signal  
Processing, Antennae,  
VLSI and IoT”



28<sup>th</sup> to 30<sup>th</sup> May 2020



Organized by

Department of Electronics  
& Communication Engineering

**Sri Vasavi Engineering College**  
(Autonomous)

Tadepalligudem, West Godavari District - 594101  
(Approved by AICTE and affiliated to JNTU Kakinada, AP)  
(Accredited by NBA - UG in EEE, ECE, CSE & ME and NAAC with 'A' Grade)  
[www.sriavasaviengg.ac.in](http://www.sriavasaviengg.ac.in)

Resource persons



Prof. I. Pratap Reddy  
Professor, Department of ACE  
JNTUH, Hyderabad



Prof. Ch. Srinivas Rao  
Professor, Department of ECE  
JNTUK, Velamangalam.



Dr. Gaurav Tiwari  
Professor, Department of EEE  
IIT Guwahati



Dr. P. Subhan Rao.  
Professor, Department of ECE  
NITW, Warangal



Dr. Anil Kumar Vuppala  
Associate Professor, PhD(KITP)  
IIT Hyderabad



Dr. R. Ramana Reddy  
Professor & HOD  
JNTU Pulvurudula

Tentative Schedule\*

Date & Day	TIMINGS	TOPIC
28.05.2020 (THURSDAY)	10.00 AM – 10.30 AM	INAUGURATION
	10.30 AM-12.00 PM	Research Challenges in Pattern Recognition and Application to Image forensics By Prof. Ch.SrinivasRao, JNTUV
	02.00 PM- 03.00 PM	Trends in Speech Processing with Machine learning By Dr. Anil Kumar Vuppala, Associate Professor, IITH
29.05.2020 (FRIDAY)	10.00 AM – 11.30 AM	Research trends in the Context of Data and IoT. By Prof. I. Pratap Reddy, Professor, JNTUH
	02.00 PM- 03.30 PM	Challenges and opportunities in the design of advanced Antenna's for practical Applications By Dr R Ramana Reddy, Professor & HOD Analog/Mixed Signal IC Design, Power Management IC Design, By Dr. P. Subhan Rao, Professor, NITW
30.05.2020 (SATURDAY)	10.00 AM- 11.30 AM	Research Challenges in VLSI By Dr. Gaurav Tiwari, Professor, IITG
	02.00 PM- 03.00 PM	Feedback and Valedictory
03.00PM – 03.30 PM		