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SRI VASAVI ENGINEERING COLLEGE

(Sponsored by Sri Vasavi Educational Society; Regd.No:898/2000)
(Autonomous)
| Accredited by NAAC with 'A' Grade | & | Accredited by NBA |
Approved by AICTE, New Delhi and Permanently Affiliated to JNTUK, Kakinada
Pedatadepalli, TADEPALLIGUDEM – 534 101, W.G. Dist, (A.P.)

Date: 20/11/2020.

Result for M.Tech II Semester (V18) Regular Examinations - September - 2020

S. No	HTNO	Course Code	Course	Grade	Credits
1.	19A81D5301	V18PST13	MODERN CONTROL THEORY	A	3
2.	19A81D5301	V18PST14	POWER SYSTEM DYNAMICS & STABILITY	A	3
3.	19A81D5301	V18PST15	SOLAR & WIND ENERGY	B	3
4.	19A81D5301	V18PST16	REAL TIME CONTROL OF POWER SYSTEMS	A	3
5.	19A81D5301	V18PST18	POWER SYSTEM DEREGULATION	A	3
6.	19A81D5301	V18PST24	HVDC & FACTS	B	3
7.	19A81D5301	V18PST42	SEMINAR-II	S	2
8.	19A81D5301	V18PSL02	POWER SYSTEMS LAB-II	S	2
9.	19A81D5801	V18CTT07	DATA SCIENCE	D	3
10.	19A81D5801	V18CTT08	ADVANCED WEB TECHNOLOGIES	F	0
11.	19A81D5801	V18CTT09	CLOUD COMPUTING	C	3
12.	19A81D5801	V18CTT10	INTERNET OF THINGS	B	3
13.	19A81D5801	V18CTT11	CYBER SECURITY	F	0
14.	19A81D5801	V18CTT17	MOBILE COMPUTING	F	0
15.	19A81D5801	V18CTT42	SEMINAR-II	A	2
16.	19A81D5801	V18CTL03	DATA SCIENCE LAB	B	1
17.	19A81D5801	V18CTL04	ADVANCED WEB TECHNOLOGIES LAB	B	1
18.	19A81D5802	V18CTT07	DATA SCIENCE	C	3
19.	19A81D5802	V18CTT08	ADVANCED WEB TECHNOLOGIES	F	0
20.	19A81D5802	V18CTT09	CLOUD COMPUTING	B	3
21.	19A81D5802	V18CTT10	INTERNET OF THINGS	B	3
22.	19A81D5802	V18CTT11	CYBER SECURITY	D	3
23.	19A81D5802	V18CTT17	MOBILE COMPUTING	C	3
24.	19A81D5802	V18CTT42	SEMINAR-II	A	2
25.	19A81D5802	V18CTL03	DATA SCIENCE LAB	B	1
26.	19A81D5802	V18CTL04	ADVANCED WEB TECHNOLOGIES LAB	A	1
27.	19A81D6802	V18VLT13	DESIGN FOR TESTABILITY	B	3
28.	19A81D6802	V18VLT14	CMOS DIGITAL IC DESIGN	B	3
29.	19A81D6802	V18VLT15	EMBEDDED SYSTEM DESIGN - II	B	3
30.	19A81D6802	V18VLT16	EMBEDDED REAL TIME SYSTEMS	B	3
31.	19A81D6802	V18VLT17	LOW POWER VLSI	B	3
32.	19A81D6802	V18VLT23	DESIGN FOR INTERNET OF THINGS	C	3

33.	19A81D6802	V18VLT42	SEMINAR-II	A	2
34.	19A81D6802	V18VLL02	EMBEDDED SYSTEM DESIGN LAB	S	2
35.	19A81D6803	V18VLT13	DESIGN FOR TESTABILITY	B	3
36.	19A81D6803	V18VLT14	CMOS DIGITAL IC DESIGN	A	3
37.	19A81D6803	V18VLT15	EMBEDDED SYSTEM DESIGN - II	A	3
38.	19A81D6803	V18VLT16	EMBEDDED REAL TIME SYSTEMS	B	3
39.	19A81D6803	V18VLT17	LOW POWER VLSI	A	3
40.	19A81D6803	V18VLT23	DESIGN FOR INTERNET OF THINGS	B	3
41.	19A81D6803	V18VLT42	SEMINAR-II	A	2
42.	19A81D6803	V18VLL02	EMBEDDED SYSTEM DESIGN LAB	S	2
43.	19A81D6804	V18VLT13	DESIGN FOR TESTABILITY	AB	0
44.	19A81D6804	V18VLT14	CMOS DIGITAL IC DESIGN	AB	0
45.	19A81D6804	V18VLT15	EMBEDDED SYSTEM DESIGN - II	AB	0
46.	19A81D6804	V18VLT16	EMBEDDED REAL TIME SYSTEMS	AB	0
47.	19A81D6804	V18VLT17	LOW POWER VLSI	AB	0
48.	19A81D6804	V18VLT23	DESIGN FOR INTERNET OF THINGS	AB	0
49.	19A81D6804	V18VLT42	SEMINAR-II	A	2
50.	19A81D6804	V18VLL02	EMBEDDED SYSTEM DESIGN LAB	A	2
51.	19A81D8701	V18SET10	FINITE ELEMENT METHOD	B	3
52.	19A81D8701	V18SET11	EARTH QUAKE RESISTANT DESIGN	B	3
53.	19A81D8701	V18SET12	STABILITY OF STRUCTURES	B	3
54.	19A81D8701	V18SET13	THEORY OF PLATES AND SHELLS	B	3
55.	19A81D8701	V18SET16	ADVANCED CONCRETE TECHNOLOGY	A	3
56.	19A81D8701	V18SET19	EARTH RETAINING STRUCTURES	B	3
57.	19A81D8701	V18SET42	SEMINAR-II	S	2
58.	19A81D8701	V18SEL02	CAD LABORATORY	S	2

Note: Last date for applying Revaluation: **25/11/2020 (Wednesday)**

Grade	Grade Points	Theory	Practical	Seminar
S	10	>=90	>=90	>=45
A	9	>=80 to <90	>=80 to <90	>=40 to <45
B	8	>=70 to <80	>=70 to <80	>=35 to <40
C	7	>=60 to <70	>=60 to <70	>=30 to <35
D	6	>=50 to <60	>=50 to <60	>=25 to <30
F	0	<50	<50	<25
AB	0	ABSENT		
MP	0	MALPRACTICE		
WH	0	WITH HELD		

PRINCIPAL