



SRI VASAVI ENGINEERING COLLEGE

(Sponsored by Sri Vasavi Educational Society; Regd.No:898/2000)

| Accredited by **NAAC** with 'A' Grade | & | Accredited by **NBA** |

Approved by AICTE, New Delhi and Permanently Affiliated to JNTUK, Kakinada
Pedatadepalli, TADEPALLIGUDEM – 534 101, W.G. Dist, (A.P.)

(Autonomous)

Result for **M.Tech I Semester (V18)** Regular Examinations - February - 2020

S. No	HTNO	Course Code	Course	Grade	Credits
1.	19A81D5301	V18PST01	POWER SYSTEM OPERATION & CONTROL	A	3
2.	19A81D5301	V18PST02	ADVANCED COMPUTER METHODS IN POWER SYSTEMS	A	3
3.	19A81D5301	V18PST03	ADVANCED POWER SYSTEM PROTECTION	A	3
4.	19A81D5301	V18PST04	MICRO CONTROLLERS AND APPLICATION	A	3
5.	19A81D5301	V18PST07	ELECTRICAL DISTRIBUTION SYSTEMS	B	3
6.	19A81D5301	V18PST10	POWER QUALITY	B	3
7.	19A81D5301	V18PST41	SEMINAR-I	S	2
8.	19A81D5301	V18PSL01	POWER SYSTEMS LAB-I	S	2
9.	19A81D5801	V18CTT01	OBJECT ORIENTED SOFTWARE ENGINEERING	C	3
10.	19A81D5801	V18CTT02	NOSQL DATABASE	C	3
11.	19A81D5801	V18CTT03	ADVANCED COMPUTER ARCHITECTURE	C	3
12.	19A81D5801	V18CTT04	ADVANCED OPERATING SYSTEMS	F	0
13.	19A81D5801	V18CTT05	ADVANCED DATA STRUCTURES AND ALGORITHM ANALYSIS	F	0
14.	19A81D5801	V18CTT06	MACHINE LEARNING	F	0
15.	19A81D5801	V18CTT41	SEMINAR-I	D	2
16.	19A81D5801	V18CTL01	NOSQL DATABASE LAB	A	1
17.	19A81D5801	V18CTL02	ADVANCED DATA STRUCTURES AND ALGORITHM ANALYSIS LAB	A	1
18.	19A81D5802	V18CTT01	OBJECT ORIENTED SOFTWARE ENGINEERING	C	3
19.	19A81D5802	V18CTT02	NOSQL DATABASE	C	3
20.	19A81D5802	V18CTT03	ADVANCED COMPUTER ARCHITECTURE	C	3
21.	19A81D5802	V18CTT04	ADVANCED OPERATING SYSTEMS	F	0
22.	19A81D5802	V18CTT05	ADVANCED DATA STRUCTURES AND ALGORITHM ANALYSIS	F	0
23.	19A81D5802	V18CTT06	MACHINE LEARNING	F	0
24.	19A81D5802	V18CTT41	SEMINAR-I	D	2
25.	19A81D5802	V18CTL01	NOSQL DATABASE LAB	S	1
26.	19A81D5802	V18CTL02	ADVANCED DATA STRUCTURES AND ALGORITHM ANALYSIS LAB	A	1
27.	19A81D6802	V18VLT01	DIGITAL SYSTEM DESIGN	A	3
28.	19A81D6802	V18VLT02	VLSI TECHNOLOGY AND DESIGN	B	3
29.	19A81D6802	V18VLT03	CMOS ANALOG IC DESIGN	B	3
30.	19A81D6802	V18VLT04	EMBEDDED SYSTEMS DESIGN-I	B	3
31.	19A81D6802	V18VLT07	SYSTEM ON CHIP	S	3
32.	19A81D6802	V18VLT10	CPLD & FPGA ARCHITECTURES AND APPLICATIONS	C	3
33.	19A81D6802	V18VLT41	SEMINAR-I	S	2
34.	19A81D6802	V18VLL01	VLSI LAB	S	2

35.	19A81D6803	V18VLT01	DIGITAL SYSTEM DESIGN	A	3
36.	19A81D6803	V18VLT02	VLSI TECHNOLOGY AND DESIGN	A	3
37.	19A81D6803	V18VLT03	CMOS ANALOG IC DESIGN	A	3
38.	19A81D6803	V18VLT04	EMBEDDED SYSTEMS DESIGN-I	B	3
39.	19A81D6803	V18VLT07	SYSTEM ON CHIP	A	3
40.	19A81D6803	V18VLT10	CPLD & FPGA ARCHITECTURES AND APPLICATIONS	B	3
41.	19A81D6803	V18VLT41	SEMINAR-I	S	2
42.	19A81D6803	V18VLL01	VLSI LAB	S	2
43.	19A81D6804	V18VLT01	DIGITAL SYSTEM DESIGN	A	3
44.	19A81D6804	V18VLT02	VLSI TECHNOLOGY AND DESIGN	B	3
45.	19A81D6804	V18VLT03	CMOS ANALOG IC DESIGN	C	3
46.	19A81D6804	V18VLT04	EMBEDDED SYSTEMS DESIGN-I	B	3
47.	19A81D6804	V18VLT07	SYSTEM ON CHIP	C	3
48.	19A81D6804	V18VLT10	CPLD & FPGA ARCHITECTURES AND APPLICATIONS	C	3
49.	19A81D6804	V18VLT41	SEMINAR-I	S	2
50.	19A81D6804	V18VLL01	VLSI LAB	A	2
51.	19A81D8701	V18MAT05	ADVANCED MATHEMATICS	B	3
52.	19A81D8701	V18SET01	THEORY OF ELASTICITY	C	3
53.	19A81D8701	V18SET02	MATRIX ANALYSIS OF STRUCTURES	C	3
54.	19A81D8701	V18SET03	STRUCTURAL DYNAMICS	S	3
55.	19A81D8701	V18SET05	SUB-STRUCTURE DESIGN	S	3
56.	19A81D8701	V18SET07	REPAIR AND REHABILITATION OF STRUCTURES	A	3
57.	19A81D8701	V18SET41	SEMINAR-I	S	2
58.	19A81D8701	V18SEL01	ADVANCED STRUCTURAL ENGINEERING LABORATORY	S	2

Note: Last date for applying Revaluation: **15/07/2020 (Wednesday)**

Grade	Grade Points	Marks Range	Course Type
S	10	>=90	P
S	10	>=45	S
S	10	>=90	T
A	9	>=80 to <89	P
A	9	>=40 to <44	S
A	9	>=80 to <89	T
B	8	>=70 to <79	P
B	8	>=35 to <39	S
B	8	>=70 to <79	T
C	7	>=60 to <69	P
C	7	>=30 to <34	S
C	7	>=60 to <69	T
D	6	>=50 to <59	P
D	6	>=25 to <29	S
D	6	>=50 to <59	T
F	0	<49	P
F	0	<25	S
F	0	<49	T

T - Theory
P - Practical
S - Seminar


PRINCIPAL

Date: 08/07/2020