

FACULTY PROFILE

Name: POLAVARAPU HARSHINI

Designation: LECTURER

Qualification: B.TECH

Date of birth: 29-01-2002

Experience: **Years**
Teaching: --- **Industrial:** --- **Research:** -----

Area of Specialization: ---

Date of joining: 12-06-2023

Research Interests: VLSI

Address for Communication:5-153 CHOPPARAMANNA GUEDEM,
KOYYALAGUEDEM MANDALAM, WEST GODAVARI DISTRICT 534312

Email:polavarapuharshini555@gmail.com

Mobile No: 9391201659

Academic Degree: (From highest)

Degree	School/College	Board/University	Year of Pass
B.TECH	SRI VASAVI ENGINEERING COLLEGE	JNTUK	2023
INTER	VSN JUNIOR COLLEGE	BOARD OF INTERMEDIATE EDUCATION	2019
SSC	AUXILIUM EM HIGH SCHOOL	SSC	2017

Administration:

Papers Published in referred Journals:

National: 1

International:0

Papers presented in Conferences:

National:1

International:0

Keynote address/Guest Lectures delivered: --

No. of Workshops / Seminars/FDPs Conducted: ----

No. of Workshops / Seminars/FDPs Participated: -----



No. of projects guided/guiding:

UG: ---

PG: ---

No. of PhD's guided/guiding: ----

Memberships in Professional Societies: ---

Achievements: (If any)

CERTIFICATION COURSE ON PYTHON PROGRAMMING LANGUAGE

PAPER PUBLISHED ON "5:2 COMPRESSOR BASED DADDA MULTIPLIER USING
VERILOG"