

FACULTY PROFILE

Name: Y. Sujatha
Designation: Sr. Assistant Professor
Qualification: M.Tech
Date of birth: 15th November
Experience: 15 Years
Teaching: 15 **Industrial:** 0 **Research:**



Area of Specialization: VLSI System Design

Date of joining: 06-06-2011

Research Interests: Low Power VLSI Design

Address for Communication: ECE Department, Sri Vasavi Engineering College,
Tadepalligudem.

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Mobile No:

Academic Degree:(From highest)

Degree	School/College	Board/University	Year of Pass
M.Tech	BVRIT, Narsapur	JNTUH	2009
B.Tech	JNTU College of Engineering, Kakinada	JNTU	2003
DECE	Govt. Polytechnic for women, Kakinada	SBTET	1997
SSC	Z.P.P High School	State Board Education	1994

Experience particulars:(latest first)

College/University	Designation	Duration
Sri Vasavi Engg. College	Sr. Assistant Professor	Jun'2011-till date
S.S Institute of Technology, Dundigal	Sr. Assistant Professor	Dec'2005-Feb'2011
S.V.E.C.W, Bhimavaram	Assistant Professor	Jun'2004-Nov'2005
Lenora College of Engineering, Rampachodavaram	Assistant Professor	Aug'2003-Jun'2004

Administration:

Papers Published in referred Journals: 07

International: 07

Papers presented in Conferences:--

National:

International:

Keynote address/Guest Lectures delivered:--

No. of Workshops / Seminars/FDPs Conducted: --

No. of Workshops / Seminars/FDPs Participated: 10

No. of projects guided/guiding:

UG: 40

PG: 5

No. of Ph.D's guided/guiding:--

Memberships in Professional Societies: ISTE

Achievements: (If any)