

FACULTY PROFILE

Name: Gopala Reddy. P

Designation: Assistant Professor

Qualification: M.Tech

Date of birth: 02-06-1979

Experience: 9Yrs.5Months

Teaching: 9Yrs.5Months

Industrial:

Research:

Area of Specialization: Embedded systems

Date of joining: 13th Sep 2010

Research Interests: VLSI & ES

Address for Communication: Dept., of ECE,
Sri Vasavi Engineering college,
Tadepalligudem, W.G (DT)-534101

Email: gopal_suryar@srivasaviengg.ac.in

Mobile No: 9866998916

Academic Degree:(From highest)

Degree	School/College	Board/University	Year of Pass
M.Tech	VEC	JNTU Hyderabad	2008
B.Tech	TPIST	JNTU Hyderabad	2005
D.Cm.E	TPP	SBTET	2001
SSC	B.P.M. High School	Board of Secondary Education	1995

Experience particulars: (latest first)

College/University	Designation	Duration
SVEC	Assistant Professor	13 th sep 2010 to Till date
NECW	Assistant Professor	2009-2010
ASR College of Engineering	Assistant Professor	2008-2009



Administration:

1. Extracurricular activity coordinator
2. Department Placement Coordinator
3. R & D Incharge
4. Faculty Profiles File (Reg. -NBA)
5. Examination Section Incharge at NECW

Papers Published in referred Journals:**National:****International: 05**

1. **P. Gopala Reddy** Implementation of Intruder Detection System Using Passive Infrared Sensor on PSoC IJMETMR,ISSN No:2348-4845, Volume No:2 (2015), Issue No: 11 (November)
2. **P. Gopala Reddy**, “MDC FFT/IFFT Processor with variable length for MIMO-OFDM systems”, IJATIR, ISSN 2348-2370, Vol 07, Issue 01, Jan 2015.
3. **P. Gopala Reddy**, “ Devise and Enlargement of a Tender-Glove banned Veer Oversee” IJEETE, Vol.43 Special Issue.3,2012
4. **P. Gopala Reddy**, “Security Management System for Oilfiled Based on GSM Technology Proceedings of the International Journal on IJCSE - Vol.3, 6th June 2011.
5. **P. Gopala Reddy**, “Automatic detection of human and Energy saving based on Zigbee Communication of the international Journal on IJCSE- Vol 3, 6th June 2011.

Papers presented in Conferences:**National:****International:****Keynote address/Guest Lectures delivered:****No. of Workshops / Seminars/FDPs Conducted:**

No. of Workshops / Seminars/FDPs Participated: 07

- **2 day National workshop on VLSI DSP system design using FPGA.** VIT University, Vellore 26th and 27th August 2016.
- **3 day International workshop on Embedded Systems** organized by Dept., of ECE, MVGR College of Engineering in associated with IUCEE during 5th -7th Jan 2015
- **2 day workshop - Recent Advances in Microwaves and Antennas & Sponsored by Department of Science and Technology(DST)-GOI, New Delhi-at SVEC Tadepalligudem during 6th and 7th sep 2014**
- **2 day workshop – Embedded Systems and Real Time Operating Systems – at GMRIT Rajam, Srikakulam (Dt) (16th &17th November 2012).**
- **3 day workshop – Antennas and Wave propagation – SVEC, Tadepalligudem (30th June to 02nd July 2011).**
- **2 day workshop – Soft Computing and Signal processing Applications to Engineering – SASI Institute of Technology , Tadepalligudem (24th & 25th June 2011).**
- **2 day workshop – FPGA based DSP Implementation using XILINX (FDIX-10) –at GMRIT, Rajam, Srikakulam (Dt) (26th & 27th November 2010).**

No. of projects guided/guiding:**UG: 16****PG: 06****No. of Ph.D's guided/guiding:****Memberships in Professional Societies:**

1. Life Member Ship in ISTE
2. Life Member Ship in IETE

Achievements: (If any)