



SRI VASAVI ENGINEERING COLLEGE (AUTONOMOUS)

(Sponsored by Sri Vasavi Educational Society)
Approved by AICTE, New Delhi and Permanently Affiliated to JNTUK, Kakinada
Pedatadepalli, **TADEPALLIGUDEM – 534 101**, W.G. Dist, (A.P.)

Department of Electronics and Communication Engineering

Dt: 10-07-2019

CIRCULAR

Bridge Course for Lateral Entry Students, B.Tech, III SEM

(18/07/2019 to 27/07/2019) Timings: 4.30PM to 6:00PM

Today we had the meeting at 2:00 P.M. with all III Sem Faculty Members and Lateral Entry (LE) students.

Problem Identified:

- The students are facing the Problem with Mathematics based Courses, Uncovered syllabus due to their late admission as well as programming languages.

Action suppose toTake:

So, we are planning to conduct some extra classes beyond the working Hours w.e.f. 18-07-2019 to 27.07.2019 as listed below

- Make up Classes on Core Courses in III semester
- Bridge Classes on some mathematics Topics like Fourier Series, Fourier Transform, Laplace transform, Z Transform
- C Programming classes beyond college hours.

Registration Link: http://srivasaviengg.ac.in/svec_events.php

Faculty Coordinator : Mr. J. Rajendra Asst.Professor, Dept. of ECE

Venue: III – A Section Class room

E. Kosmatine

Head of the Department