

**SRI VASAVI ENGINEERING COLLEGE****(AUTONOMOUS)**

(Sponsored by Sri Vasavi Educational Society)

Approved by AICTE, New Delhi and Permanently Affiliated to JNTUK, Kakinada  
Pedatadepalli, **TADEPALLIGUDEM – 534 101**, W.G. Dist, (A.P.)**Department of Electronics and Communication Engineering**

Date: 15.7.2019

**CIRCULAR**

ECE department is organizing a three day workshop on SCI Lab in association with APSSDC for III Year ECE Students. The SCI lab is a high level numerically oriented programming language and used for signal processing. The details of the workshop are as follows.

Schedule:

Sl.No.	Class	Dates	Time:
1	III ECE A Sec	22.7.2019 to 24.7.2019	9.30 A.M TO 4.30 P.M
2	III ECE B Sec	25.7.2019 to 27.7.2019	9.30 A.M TO 4.30 P.M

Registration Fee: Rs.150 per Student.Venue: DSP Lab ECE block

All the students are informed to utilize this opportunity to learn about SCI Lab and attend the workshop without fail as per the Schedule.

Faculty coordinators: 1.Dr.Yedukondalu Associate Professor/ECE  
2. Dr.M.Thamarai Professor/ECE

HOD/ECE