



SRI VASAVI ENGINEERING COLLEGE

(Approved by AICTE, New Delhi & Affiliated to JNTU, Kakinada)

Pedatadepalli, TADEPALLIGUDEM-534 101, W.G. Dist., Andhrapradesh

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Accredited
By
NBA

ACADEMIC YEAR 2015-2016 (April 2015 – March 2016)

Sno	Name of the Faculty	Name of the Workshop	Host institution	Date	Year	Cost (in RS)
1	D.Rajesh Setty	“Engineering Education & Research Seminar”	National Instruments India Kakinada	19 th August	2015	----
2	P.Murali Krishna	“Low Power System On Chip Design using XILINX FPGA Devices”	National Institute of Technology Warangal	3 rd -7 th October	2015	----
3	Sri KSS Kiran	“Low Power System On Chip Design using XILINX FPGA Devices”	National Institute of Technology Warangal	3 rd -7 th October	2015	----
4	Ch.Jyotsna Rani	“Analog and Digital CMOS IC Design Using Mentor Graphics EDA Tools”	Shri Vishnu Engineering College for Women	5 th -6 th October	2015	350
5	T.N.V.Prathyusha	“Analog and Digital CMOS IC Design Using Mentor Graphics EDA Tools”	Shri Vishnu Engineering College for Women	5 th -6 th October	2015	350
6	V.Radha	“Analog and Digital CMOS IC Design Using Mentor Graphics EDA Tools”	Shri Vishnu Engineering College for Women	5 th -6 th October	2015	350